PRODUCTION SPECIFICATION OF LCD MODULE

MODULE NO.: XY040BHD8685CCT

Customer Name:		
Customer Part Number:		
Approved By:	Date:	

Prepared By	Checked By	Approved By



Table of Contents

Re	vision History	3
	General Specifications	
	Pin Assignment	
	Absolute Maximum Ratings	
4	Electrical Characteristics	
5	INTERFACE TIMING	8
	5.1 System Bus Read/Write Characteristics.	8
	5.2 Power ON/OFF Timing	10
6	Optical Characteristics	11
7	Environmental / Reliability Test	
8	Mechanical Drawing	16
9	Precautions For Use of LCD Modules	



Revision History

Rev	Issued Date	Description	Page	Editor
1.0	Dec. 15,2023	First release	All	

www.szxot.com 3 / 17 Dec. 15,2023 Rev. 1.0



1 General Specifications

	Feature	Specifications
	LCD type	4.0 inch
	Resolution (H*V)	720(RGB) *720
	Technology Type	a-Si TFT
Display Spec.	Pixel Configuration	R.G.B. Vertical Stripe
Display Opec.	Display Mode	IPS/Transmissive/Normally Black
	Viewing Direction	All
	Gray Scale Inversion Direction	
	OutlineDimensions (W x H x T) (mm)	126.44(H) *126.44(V)*3.85(T)
	Active Area(mm)	101.52 (H)*101.52(V)
Mechanical	With /Without Touch screen	With CTP
Characteristics	Match Connector Type	0.5 Pitch 30 Pin
	Backlight Type	White LED
	Weight (g)	TBD
F 1(:)	Interface	MIPI 4Lane
Electrical Characteristics	Number of color	16.7M
O i la la cier i sti cs	Driver IC	FL7707

www.szxot.com 4 / 17 Dec. 15,2023 Rev. 1.0



2 Pin Assignment

NO.	PIN NAME	Description
1	LEDA	LED anode
2	LEDK1	LED Cathode
3	LEDK2	LED Cathode
4	VCI	Power Supply 2.8~3.3V
5	IOVCC	Power Supply 1.8V-2.0V
6	RESET	LCM reset signals
7	TE	Tearing effect output
8	PWM	The PWM frequency output for LCD driver control.
9	GND	Ground
10	D0P	DSI-D0+ data signals
11	D0N	DSI-D0- data signals
12	GND	Ground
13	D1P	DSI-D1+ data signals
14	D1N	DSI-D1- data signals
15	GND	Ground
16	CLKP	DSI-Clock+
17	CLKN	DSI-Clock-
18	GND	Ground
19	D2P	DSI-D2+ data signals
20	D2N	DSI-D2- data signals
21	GND	Ground
22	D3P	DSI-D3+ data signals
23	D3N	DSI-D4- data signals
24	GND	Ground
25	TP_INT	Touch Interrupt
26	TP_SDA	Touch IIC Data signal
27	TP_SCL	Touch IIC Clock signal
28	TP_RESET	Touch Reset Signal
29	TP_VCI	Touch Power supply
30	TP_IOVCC	Touch Power supply

www.szxot.com 5 / 17 Dec. 15,2023 Rev. 1.0



3 Absolute Maximum Ratings

GND=0V, Ta= 25°C

Item	Symbol	Value	Unit
Power supply voltage for logic	V _{DD}	0.3~3.6	V
Input voltage	Vin	V _{DD} +0.3	V
Operating temperature	Topr	-20 to 70	. C
Storage temperature	Tstg	-30 to 80	. C

4 Electrical Characteristics

4.1 Driving TFT LCD Panel

GND=0V, Ta=25℃

Item	Symbol	Min	Type	Max	Unit	Test condition
Operating voltage	V _{DD}	2.6	2.8	3.3	V	-
Supply current	lod	-	-	30	mA	V _{DD} =2.8V,Ta=25。C
	VIH	0.8VDD	-	VDD	V	
Input voltage	VIL	0	-	0.2VDD	V	-
Input leakage current	lıL	-1.0	-	1.0	μΑ	V _{IN} =V _{DD} or V _{SS}

Note: Voltage greater than above may damage the module.

All voltages are specified relative to VSS=0V.

4.2 Driving Backlight

Ta=25°C

Item	Symbol	Min	Тур	Max	Unit	Remark
Forward Current	l F		40	_	mΑ	
Forward Voltage	V _F	-	30	_	٧	
Connection mode	Р		5S2P			
LED number	/		10		pcs	
LED life time		20000			hours	

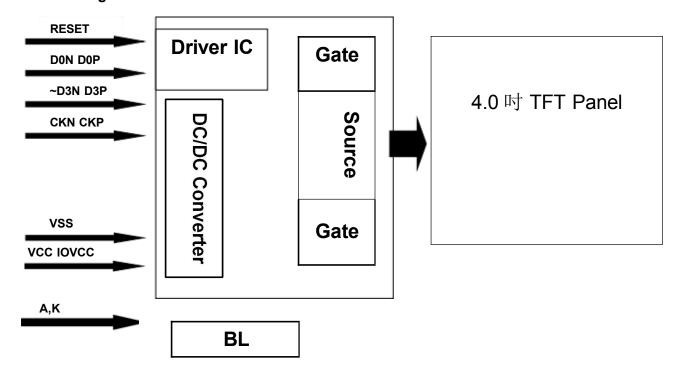
Note1: Optical performance should be evaluated at Ta=25 $^{\circ}$ C only .If LED is driven by high current, high ambient temperature & humidity condition. The life time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.

LED CIRCUIT DIAGRAM:

www.szxot.com 6 / 17 Dec. 15,2023 Rev. 1.0



4.3 Block Diagram



www.szxot.com 7 / 17 Dec. 15,2023 Rev. 1.0



5 INTERFACE TIMING

5.1 DSI Interface Timing Characteristics.

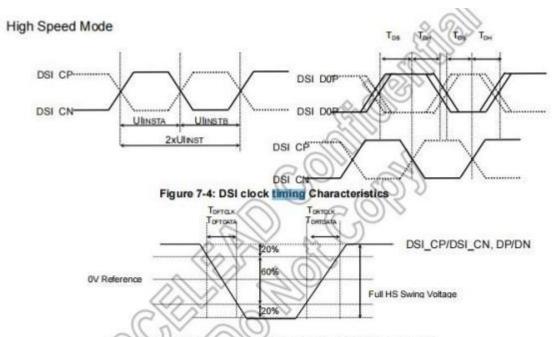


Figure 7-5: Rising and falling time on clock and data channel

DSI_CN DP/DN DSI_CP/	Item	Cumbal	Sp	Unit		
	item	Symbol	Min.	Тур.	Max.	Unit
DSI_CP/ DSI_CN	Double UI instantaneous	2xUinst	4LANE: 3.30 3LANE: 2.85 @ VDDD=1.8V	-	- 25	
	UI instantaneous Uins		4LANE: 1.67 3LANE: 1.43 @ VDDD=1.8V	3.83	12.5	ns
DD/DN	Data to clock setup time	Tos	0.15xUI			ps
DP/DN	Data to clock hold time		0.15xUI	-	-	ps
DSI_CP/	Differential rise time for clock	TORTCLK	150	-	0.3UI	ps
OSI_CN	Differential fall time for dock Torre		150		0.3UI	ps
DP/DN	Differential rise time for data	TORTDATA	150	-	0.3UI	ps
DHIDN	Differential fall time for data	Toftdata	150	-	0.3UI	ps

Rising and falling time on clock and data channel

www.szxot.com 8 / 17 Dec. 15,2023 Rev. 1.0



Low Power Mode

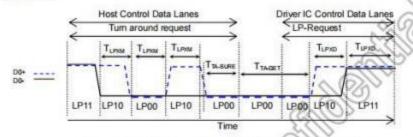


Figure 7-6: BTA from HOST to Display Module Timing

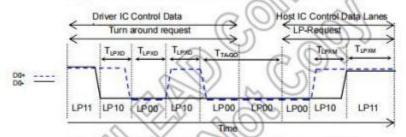


Figure 7-7: BTA from Display Module Timing to HOST

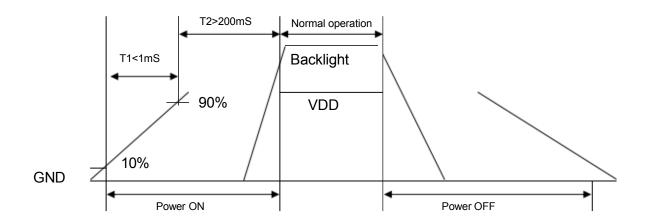
(VSSA=0V, IOVCC=1.65V to 3.3V, VCI=2.3V to 3.3V, TA = -40 to 85°C)

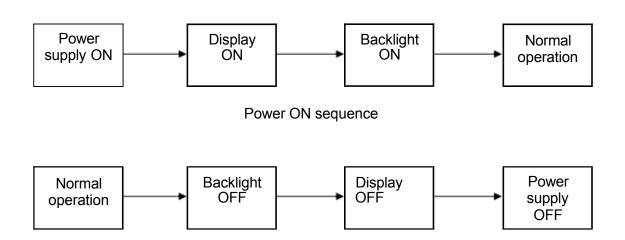
Cinnel	10	Cumbal		Halt		
	Item	Symbol	Min.	Тур.	Max.	Unit
	Length of LP-00/LP01/LP10/LP11 Host→ Display module	Тьрхм	50			ns
OSI DOP/	Length of LP-00/LP01/LP10/LP11 Display module →Host	TLPXD	50	*	-	ns
DSI_DOP	Time-out before the MPU start driver	T _{TA-SURE}	TLPXD		2xTLPXD	ns
	Time to drive LP-00 by display module	TTAGET	5xTLPXD	-	-	ns
	Time to drive LP-00 after turnaround request Host	Ттадо	4xTLPXD	23	1943	ns

www.szxot.com 9 / 17 Dec. 15,2023 Rev. 1.0



5.2 Power ON/OFF Timing





Power OFF sequence

www.szxot.com 10 / 17 Dec. 15,2023 Rev. 1.0



6 Optical Characteristics

Ta=25℃

Item	Symbol	Condition	Min	Тур	Max	Unit	Remark
	θТ			85	I		
Viou Angloo	θВ	OD > 10		85	I	Dograd	Note 2
View Angles	θL	CR≥10		85	I	Degree	Note 2
	θR			85	I		
Contrast Ratio	CR	θ=0°	800	1000	ı	-	Note1 Note3
Dognanaa Tima	Ton	0 5°○	-	25	35	ms	Note1
Response Time	Toff	25℃					Note4
Uniformity	U	-	-	-	1	%	Note1 Note6
NTSC	-	-	63	68	ı	%	Note 5
Luminance	L	-	-	450	ı	cd/m²	Note1 Note7

Test Conditions:V_F=30 V, I_F=40mA, the ambient temperature is 25° C.

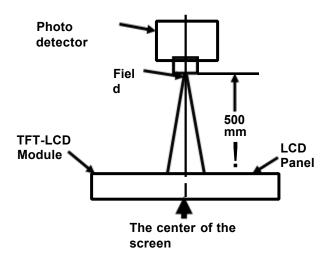
1. The test systems refer to Note 1 and Note 2.

www.szxot.com 11 / 17 Dec. 15,2023 Rev. 1.0



Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Item	Photo detector	Field
Contrast Ratio		1°
Luminance	SR-3A	
Chromaticity	SR-SA	
Lum Uniformity		
Response Time	BM-7A	2°

Note 2: Definition of viewing angle range and measurement system. viewing angle is measured at the center point of the LCD by CONOSCOPE(ergo-80).

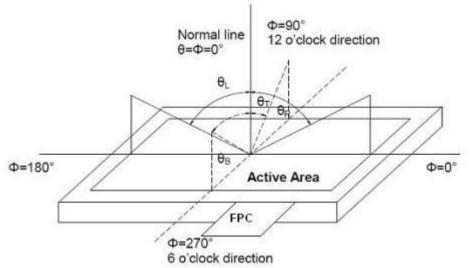


Fig. 1 Definition of viewing angle

www.szxot.com 12 / 17 Dec. 15,2023 Rev. 1.0



Note 3: Definition of contrast ratio

Contrast ratio (CR) = Luminance measured when LCD is on the "White" state

Luminance measured when LCD is on the "Black" state

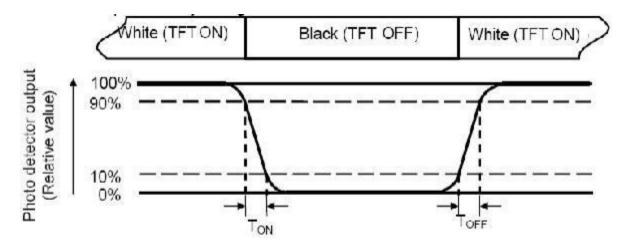
"White state ": The state is that the LCD should be driven by Vwhite.

"Black state": The state is that the LCD should be driven by Vblack.

Vwhite: To be determined Vblack: To be determined.

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (TON) is the time between photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931) Color coordinates measured at center point of LCD.

www.szxot.com 13 / 17 Dec. 15,2023 Rev. 1.0



Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (U) = Lmin/Lmax

L-----Active area length W----- Active area width

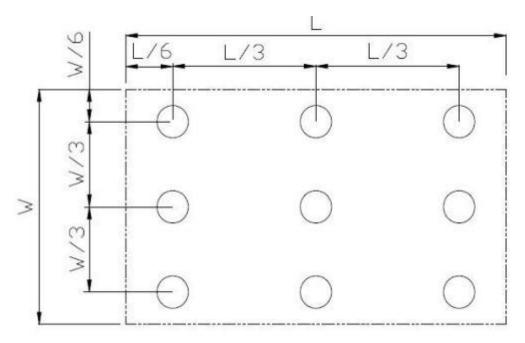


Fig. 2 Definition of uniformity

Lmax: The measured maximum luminance of all measurement position.

Lmin: The measured minimum luminance of all measurement position.

Note 7: Definition of Luminance:

Measure the luminance of white state at center point.

www.szxot.com 14 / 17 Dec. 15,2023 Rev. 1.0



7 Environmental / Reliability Test

Item	Condition	Time (hrs)	Assessment
High temp. Storage	80, C	120	
High temp. Operating	70。C	120	
Low temp. Storage	-30° C	120	
Low temp. Operating	-20 _° C	120	No abnormalities in functions
Humidity	60。C/ 90%RH	120	and appearance
Thermal Shock(Non-operation)	-20 _o C ← 25 _o C → 70 _o C (0.5 hour ← 5 min → 0.5 hour)	10cycles	

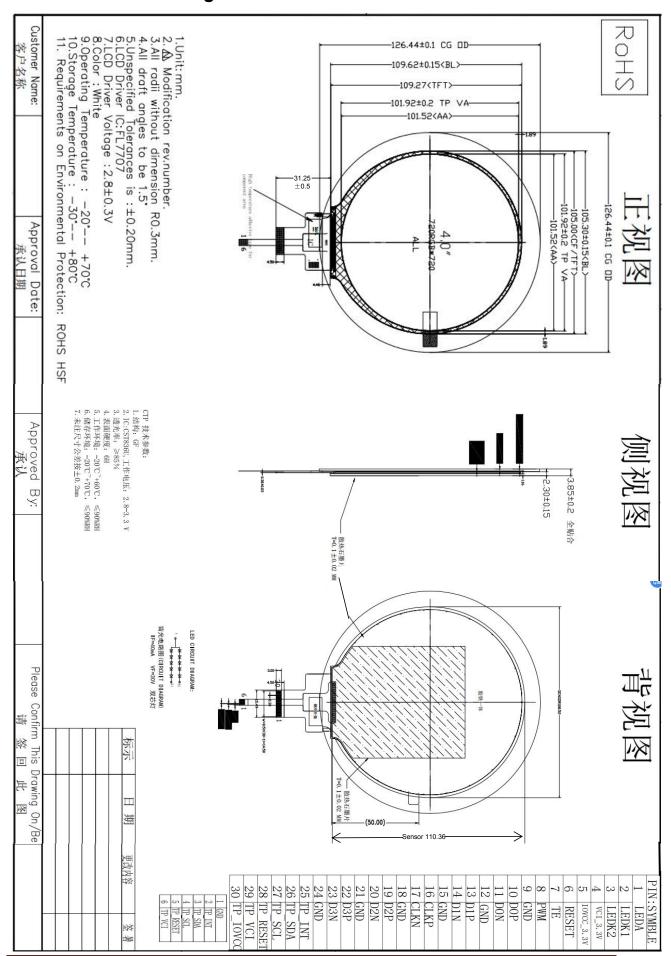
Remark:

- 1. The test samples should be applied to only one test item.
- 2. Sample size for each test item is 1~10pcs.
- 3.In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.

www.szxot.com 15 / 17 Dec. 15,2023 Rev. 1.0



8 Mechanical Drawing





9 Precautions For Use of LCD Modules

- **9.1** Handling Precautions
- 9.1.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 9.1.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 9.1.1.3 Do not apply eXYessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 9.1.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 9.1.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
 - Isopropyl alcohol
 - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents
- 9.1.1.6 Do not attempt to disassemble the LCD Module.
- 9.1.1.7 If the logic circuit power is off, do not apply the input signals.
- 9.1.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
- 9.1.1.9 Be sure to ground the body when handling the LCD Modules.
- 9.1.1.10 Tools required for assembly, such as soldering irons, must be properly ground.
- 9.1.1.11 To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
- 9.1.1.12 The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.
- 9.1.1.13 Storage precautions
- 9.1.1.14 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 9.1.1.15 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:
- 9.1.1.16 The LCD modules should be stored in the room without acid, alkali and harmful gas.
- **9.2** Transportation Precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid eXYessive press, water, damp and sunshine.

www.szxot.com 17 / 17 Dec. 15,2023 Rev. 1.0