



上海捷瑞德半导体
Jerrett Semiconductor

JRG40T120FDH

IGBT

1. Description

JRG40T120FDH is obtained by advanced Trench Field Stop (T-FS) technology which reduces the conduction loss, improves switching performance, and enhances the avalanche energy. The IGBT is suitable device for UPS, Welding, and high-speed switching.

KEY CHARACTERISTICS

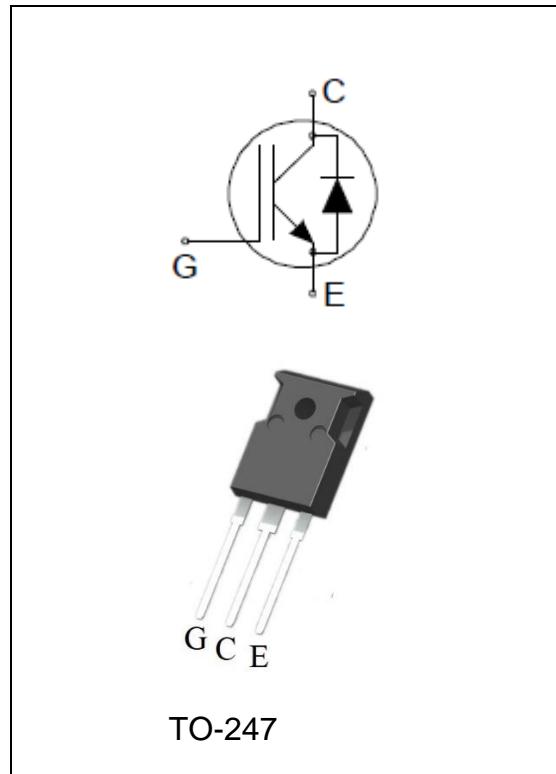
Parameter	Value	Unit
V_{CES}	1200	V
I_c	40	A
$V_{CE(sat).typ}$	2.0	V

FEATURES

- Fast Switching
- Low $V_{CE(sat)}$
- Positive temperature coefficient
- Very soft, fast recovery anti-parallel diode
- RoHS product

APPLICATIONS

- UPS
- Welding Converters
- Converters with high switching frequency



ORDERING INFORMATION

Ordering Codes	Package	Product Code	Packing
JRG40T120FDH-F	TO-247	G40T120FDH	Tube

JRG40T120FDH-F (1) JRG40T120FDH: 1200V 40A (2) F:TO-247		XXXX: Product Code YYWW: Year & Week ZZ: Assembly Code SSSS: Lot Code
--	--	--



上海捷瑞德半导体
Jerrett Semiconductor

JRG40T120FDH

IGBT

2. ABSOLUTE RATINGS

Symbol	Parameter	Values	Units
V_{CES}	Collector-Emitter Voltage	1200	V
I_C	Collector Current @ $T_c=25^\circ C$	80	A
	Collector Current @ $T_c=100^\circ C$	40	A
I_{CM}	Pulsed Collector Current, tp limited by T_{Jmax}	160	A
I_F	Diode Continuous Forward Current @ $T_c=25^\circ C$	80	A
	Diode Continuous Forward Current @ $T_c=100^\circ C$	40	A
I_{FM}	Diode Maximum Forward Current, limited by T_{Jmax}	160	A
V_{GES}	Gate-Emitter Voltage	± 20	V
t_{sc}	Short circuit withstand time $V_{GE}=15V$, $V_{CC}\leq 600V$, Allowed number of short circuits < 1000, Times between short circuits: $\geq 1.0s$, $T_J \leq 150^\circ C$	10.0	μs
P_D	Power Dissipation @ $T_c=25^\circ C$	367	W
T_{Jmax}, T_{stg}	Operating Junction and Storage Temperature Range	150, -55 to 150	$^\circ C$
T_L	Maximum Temperature for Soldering	260	$^\circ C$

3. Thermal characteristics

Symbol	Parameter	Values	Units
$R_{\theta JC}$	Junction-to-Case (IGBT)	0.34	$^\circ C/W$
$R_{\theta JC}$	Junction-to-Case (Diode)	0.8	$^\circ C/W$
$R_{\theta JA}$	Junction-to-Ambient	40	$^\circ C/W$

4. Electrical Characteristics

at $T_c = 25^\circ C$, unless otherwise specified

Static Characteristics

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
V_{CES}	Collector-Emitter Breakdown Voltage	$V_{GE} = 0V$, $I_C = 250\mu A$	1200	--	--	V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$V_{GE} = 15V$, $I_C = 40A$ $T_J=25^\circ C$ $T_J=100^\circ C$ $T_J=150^\circ C$	--	2.0 2.25 2.35	2.4	V



上海捷瑞德半导体
Jerrett Semiconductor

JRG40T120FDH

IGBT

$V_{GE(TH)}$	Gate Threshold Voltage	$V_{CE} = V_{GE}, I_C = 1\text{mA}$	5.0	5.8	6.5	V
V_F	Diode Forward Voltage	$I_F=20\text{A}$ $T_J=25^\circ\text{C}$ $T_J=100^\circ\text{C}$ $T_J=150^\circ\text{C}$	--	1.60	2.20	V
V_F	Diode Forward Voltage	$I_F=40\text{A}$ $T_J=25^\circ\text{C}$ $T_J=100^\circ\text{C}$ $T_J=150^\circ\text{C}$	--	1.85	2.45	V
I_{CES}	Collector-Emitter Leakage Current	$V_{CE} = 1200\text{V},$ $V_{GE} = 0\text{V}$	--	--	10	μA
$I_{GES(F)}$	Gate-Emitter Forward Leakage Current	$V_{GE} = +20\text{V}$	--	--	600	nA
$I_{GES(R)}$	Gate-Emitter Reverse Leakage Current	$V_{GE} = -20\text{V}$	--	--	-600	nA
Pulse width $tp \leq 300\mu\text{s}$, $\delta \leq 2\%$						

Dynamic Characteristics

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
C_{iss}	Input Capacitance	$V_{GE}=0\text{V}$ $V_{CE}=25\text{V}$ $f = 1.0\text{MHz}$	--	3640	--	pF
C_{oss}	Output Capacitance		--	168	--	
C_{rss}	Reverse Transfer Capacitance		--	89	--	
Q_G	Gate charge	$V_{cc}=960\text{V}$ $I_{CE}=40\text{A}$ $V_{GE}=15\text{V}$	--	259	--	nC
Q_{GE}	Gate-emitter charge		--	19	--	
Q_{GC}	Gate-collector charge		--	181	--	
$I_{C(SC)}$	Short circuit collector current Max.1000 short circuits, Times between short circuits: $\geq 1.0\text{s}$	$V_{GE}=15.0\text{V}, V_{cc}\leq 600\text{V},$ $t_{sc}\leq 10\mu\text{s}, T_J\leq 150^\circ\text{C}$		160		A

IGBT Switching Characteristics, at $T_J=25^\circ\text{C}$

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
$t_{d(on)}$	Turn-on Delay Time	$I_C = 40\text{A}$ $V_{cc} = 600\text{V}$ $V_{GE} = 15\text{V}$ $R_G = 10\Omega$ $T_J = 25^\circ\text{C}$ Inductive Load	--	43	--	ns
t_r	Rise Time		--	74	--	
$t_{d(off)}$	Turn-Off Delay Time		--	285	--	
t_f	Fall Time		--	30	--	
E_{on}	Turn-On Switching Loss		--	5.0	--	mJ



上海捷瑞德半导体
Jerrett Semiconductor

JRG40T120FDH

IGBT

E_{off}	Turn-Off Switching Loss		--	1.5	--	
E_{ts}	Total Switching Loss		--	6.5	--	

IGBT Switching Characteristics, at $T_J=150^\circ C$

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
$t_{d(on)}$	Turn-on Delay Time	$I_C = 40A$ $V_{CC} = 600V$ $V_{GE} = 15V$ $R_G = 10\Omega$ $T_J = 150^\circ C$ Inductive Load	--	40	--	ns
t_r	Rise Time		--	73	--	
$t_{d(off)}$	Turn-Off Delay Time		--	345	--	
t_f	Fall Time		--	86	--	
E_{on}	Turn-On Switching Loss		--	5.2	--	mJ
E_{off}	Turn-Off Switching Loss		--	2.7	--	
E_{ts}	Total Switching Loss		--	7.9	--	

Diode Characteristics, at $T_J=25^\circ C$

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
T_{rr}	Reverse Recovery Time	$I_F = 20A$, $dI/dt = 200A/\mu s$, $T_J = 25^\circ C$	--	40	--	ns
Q_{rr}	Reverse Recovery Charge		--	468	--	nC
I_{rrm}	Reverse Recovery Current		--	8	--	A
T_{rr}	Reverse Recovery Time	$I_F = 40A$, $dI/dt = 200A/\mu s$, $T_J = 25^\circ C$	--	44	--	ns
Q_{rr}	Reverse Recovery Charge		--	598	--	nC
I_{rrm}	Reverse Recovery Current		--	10	--	A

Diode Characteristics, at $T_J=150^\circ C$

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
T_{rr}	Reverse Recovery Time	$I_F = 20A$, $dI/dt = 200A/\mu s$, $T_J = 150^\circ C$	--	53	--	ns
Q_{rr}	Reverse Recovery Charge		--	492	--	nC
I_{rrm}	Reverse Recovery Current		--	17	--	A
T_{rr}	Reverse Recovery Time	$I_F = 40A$, $dI/dt = 200A/\mu s$, $T_J = 150^\circ C$	--	63	--	ns
Q_{rr}	Reverse Recovery Charge		--	660	--	nC
I_{rrm}	Reverse Recovery Current		--	18	--	A



上海捷瑞德半导体
Jerrett Semiconductor

JRG40T120FDH
IGBT

5. Characteristics Curves

Figure 1. Forward Bias Safe Operating Area

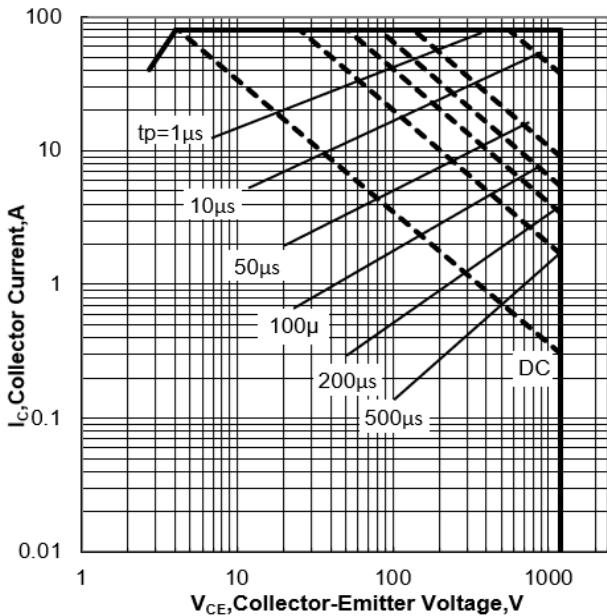


Figure 2. Power Dissipation vs Case Temperature

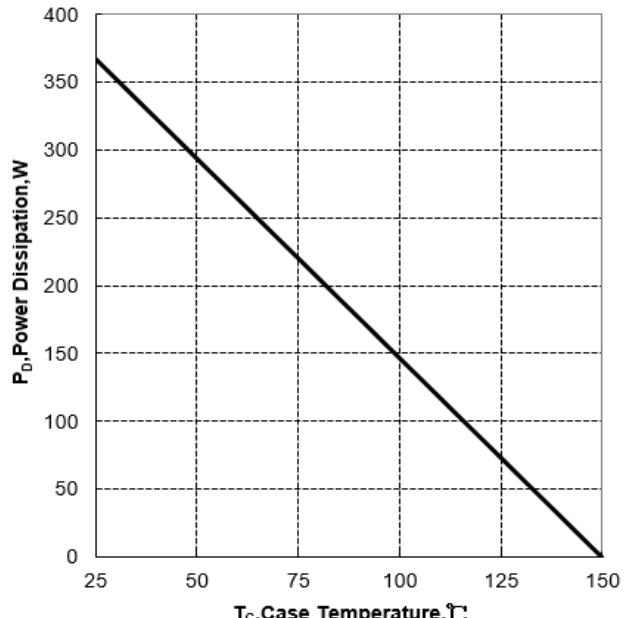


Figure 3. Collector Current vs Case Temperature

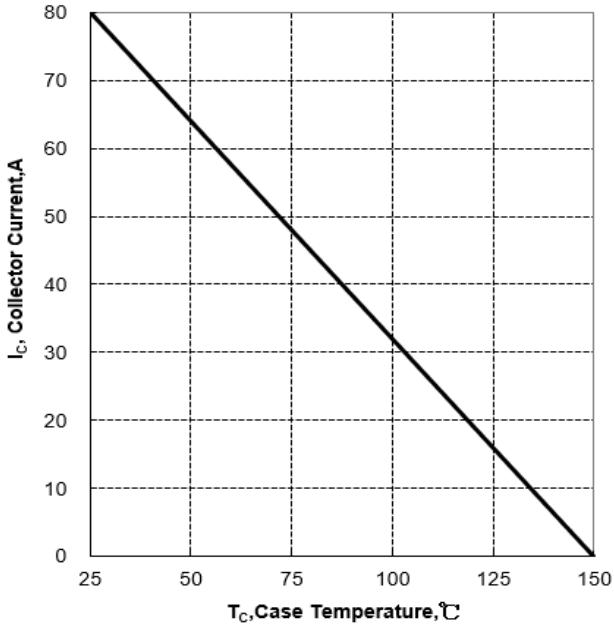
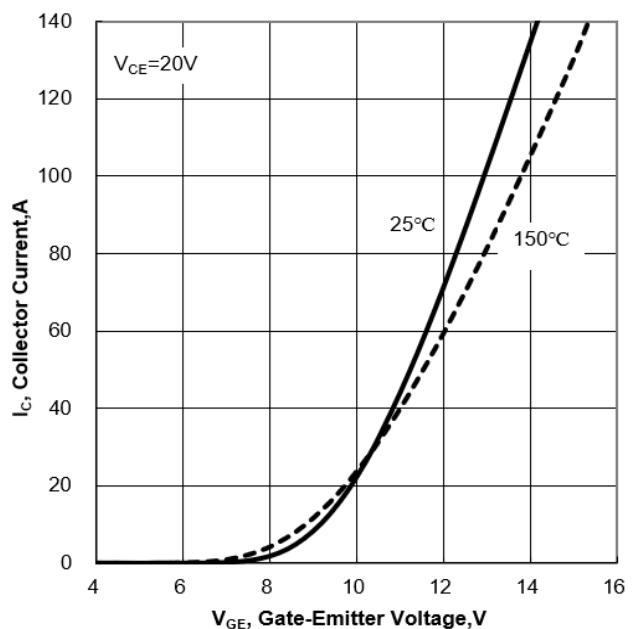


Figure 4. Typical Transfer Characteristics





上海捷瑞德半导体
Jerrett Semiconductor

JRG40T120FDH

IGBT

Figure 5. Typical Output Characteristics
($T_J=25^\circ\text{C}$)

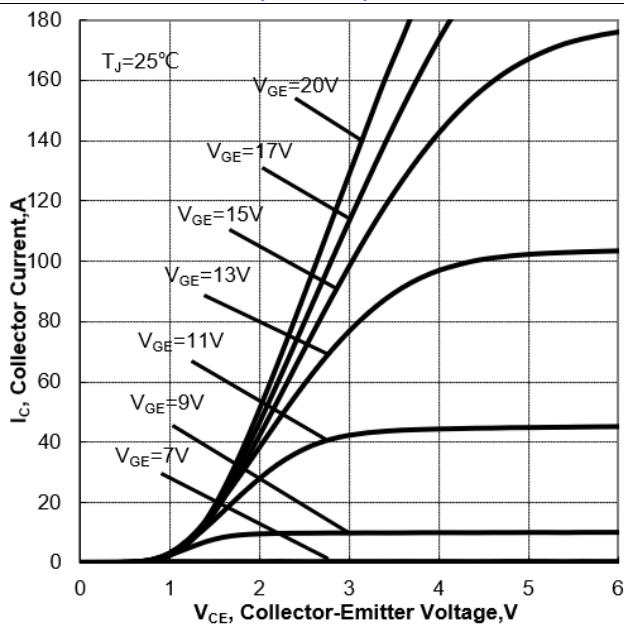


Figure 6. Typical Output Characteristics
($T_J=150^\circ\text{C}$)

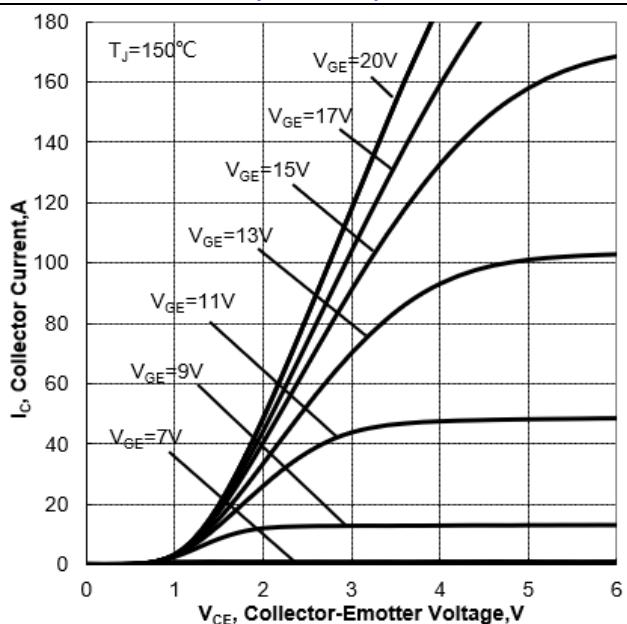


Figure 7. Typical Collector-Emitter Saturation
Voltage vs Junction Temperature

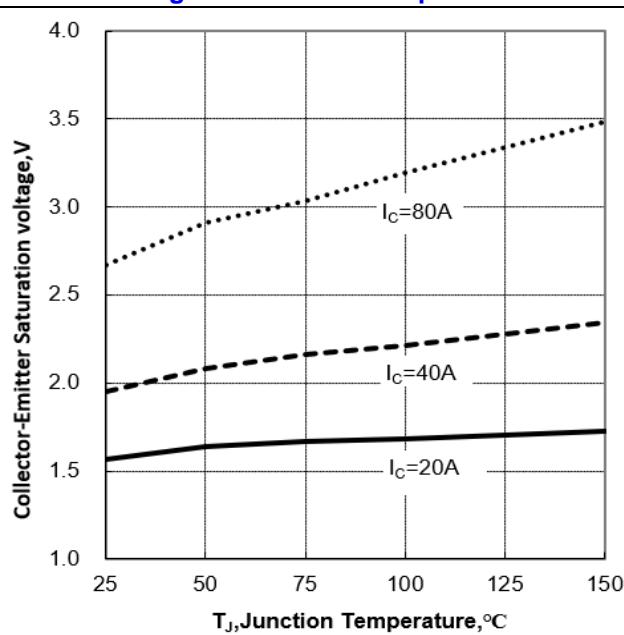
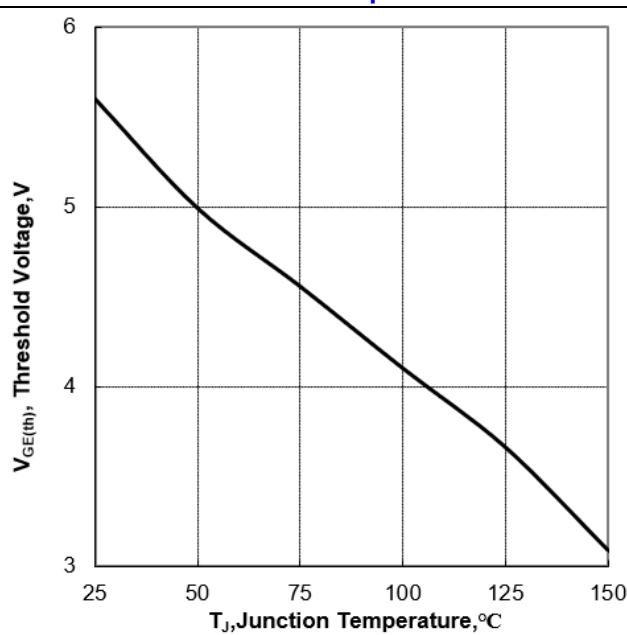


Figure 8. Typical Gate-Emitter Threshold Voltage
vs Junction Temperature





上海捷瑞德半导体
Jerrett Semiconductor

JRG40T120FDH
IGBT

Figure 9. Typical Switching Times vs Gate Resistor ($T_J=25^\circ\text{C}$, $V_{CE}=600\text{V}$, $V_{GE}=15/0\text{V}$, $I_c=40\text{A}$)

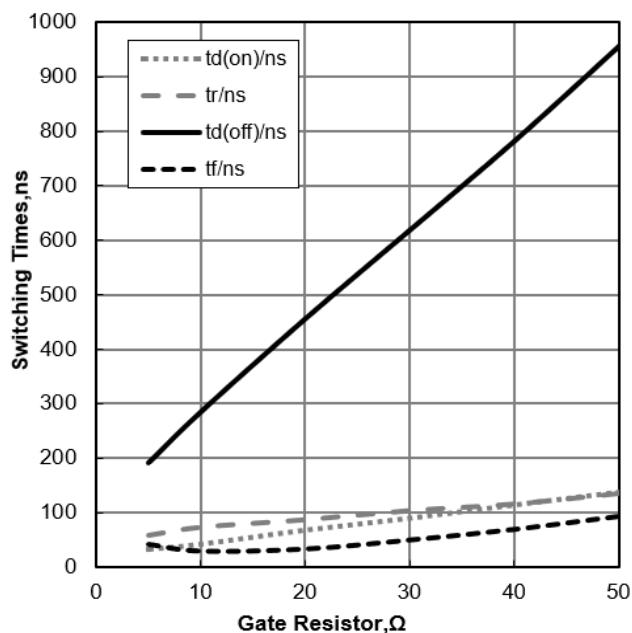


Figure 10. Typical Switching Energy vs Gate Resistor ($T_J=25^\circ\text{C}$, $V_{CE}=600\text{V}$, $V_{GE}=15/0\text{V}$, $I_c=40\text{A}$)

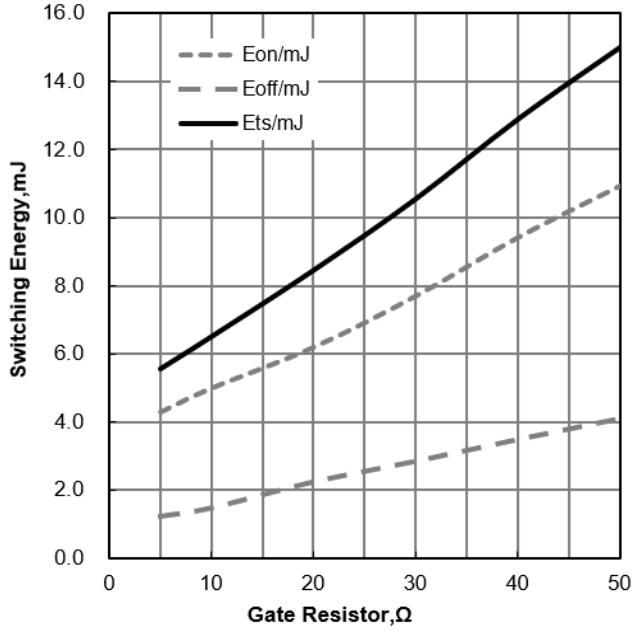


Figure 11. Typical Switching Times vs Junction Temperature ($V_{CE}=600\text{V}$, $V_{GE}=15/0\text{V}$, $I_c=40\text{A}$)

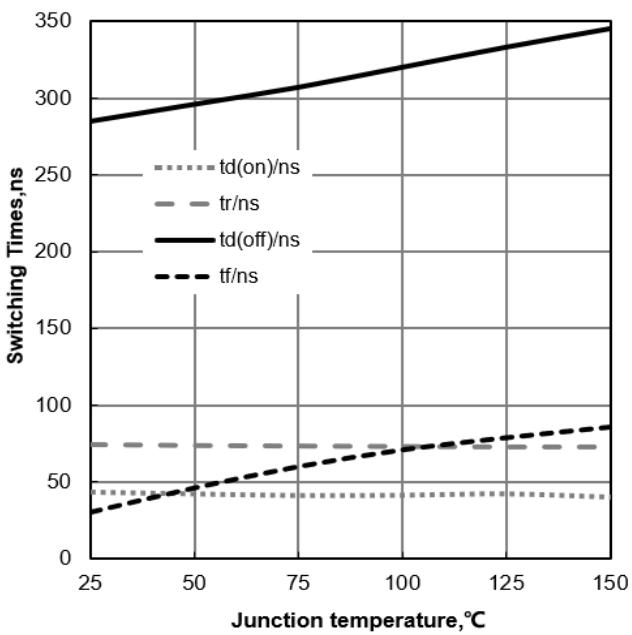
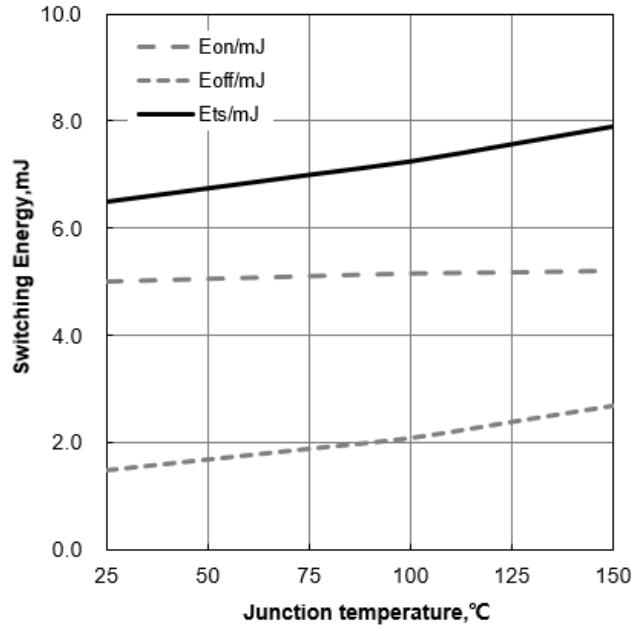


Figure 12. Typical Switching Energy vs Junction Temperature ($V_{CE}=600\text{V}$, $V_{GE}=15/0\text{V}$, $I_c=40\text{A}$)





上海捷瑞德半导体
Jerrett Semiconductor

JRG40T120FDH

IGBT

Figure 13. Typical Switching Times vs Collector Current ($T_J=25^\circ\text{C}$, $V_{CE}=600\text{V}$, $V_{GE}=15/0\text{V}$)

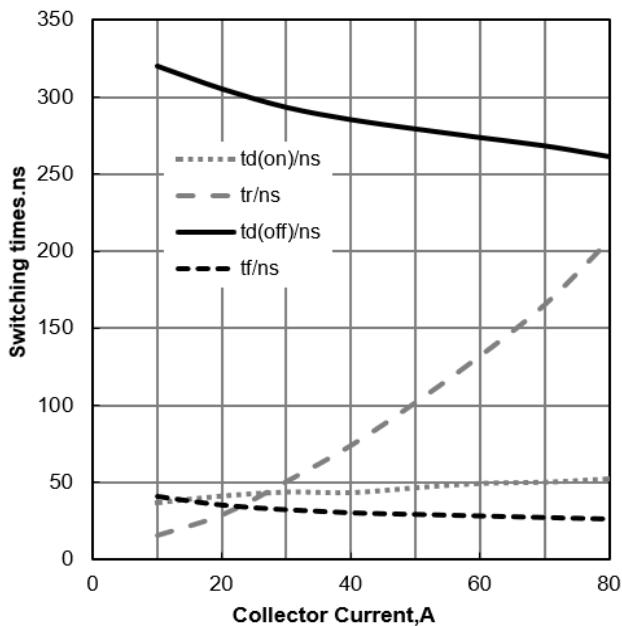


Figure 14. Typical Switching Energy vs Collector Current ($T_J=25^\circ\text{C}$, $V_{CE}=600\text{V}$, $V_{GE}=15/0\text{V}$)

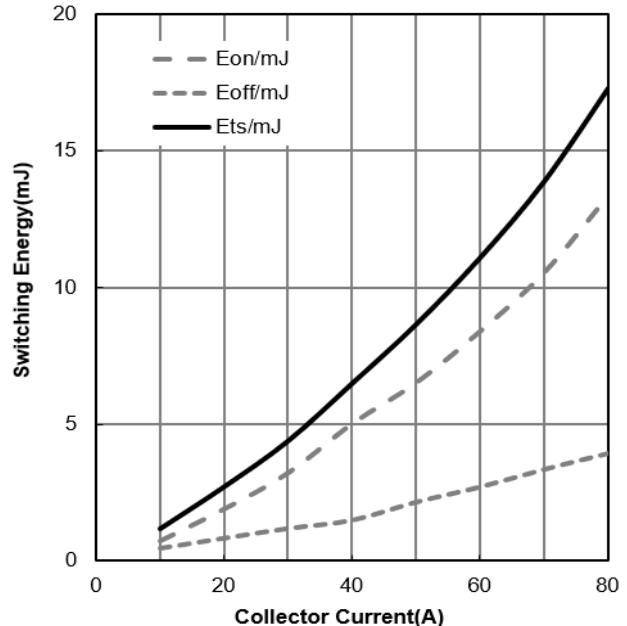


Figure 15. Typical Switching Times vs V_{CE} ($T_J=25^\circ\text{C}$, $V_{GE}=15/0\text{V}$, $I_c=40\text{A}$)

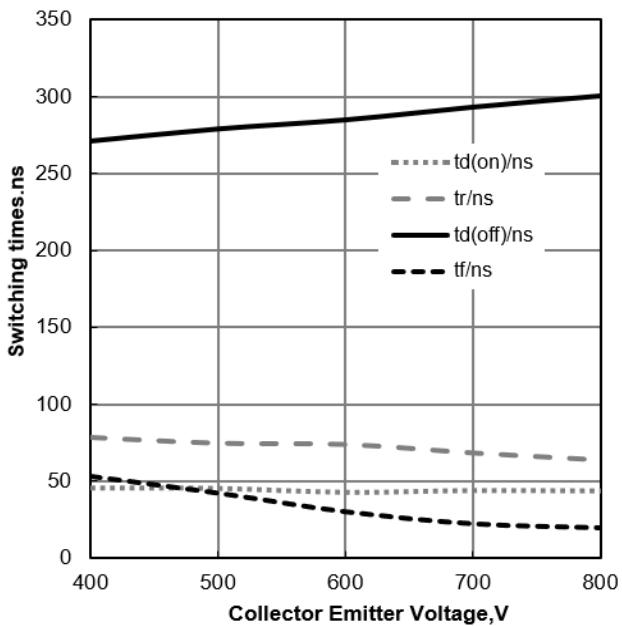
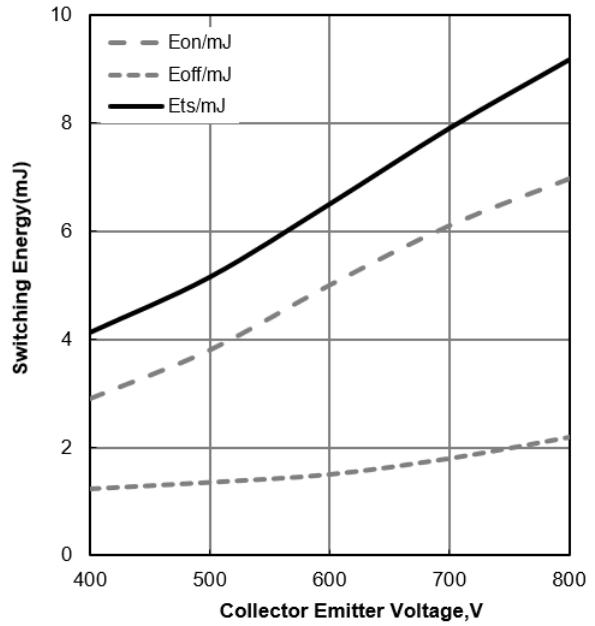


Figure 16. Typical Switching Energy vs V_{CE} ($T_J=25^\circ\text{C}$, $V_{GE}=15/0\text{V}$, $I_c=40\text{A}$)





上海捷瑞德半导体
Jerrett Semiconductor

JRG40T120FDH
IGBT

Figure 17. Typical Gate Charge

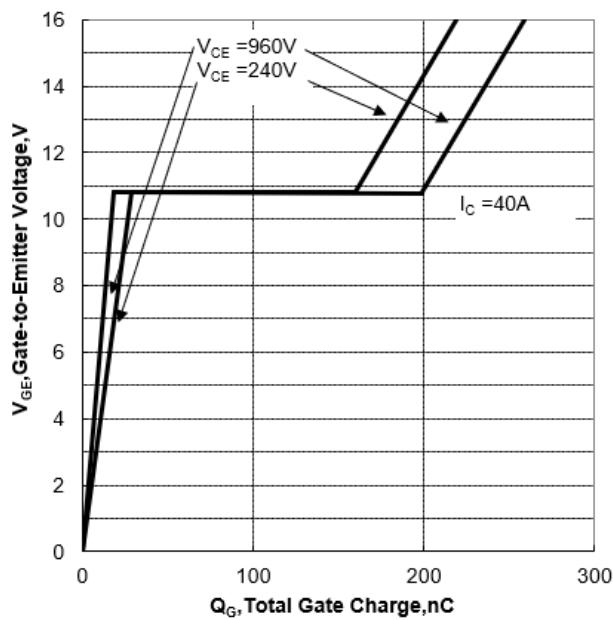


Figure 18. Typical Capacitance vs Collector-Emitter Voltage

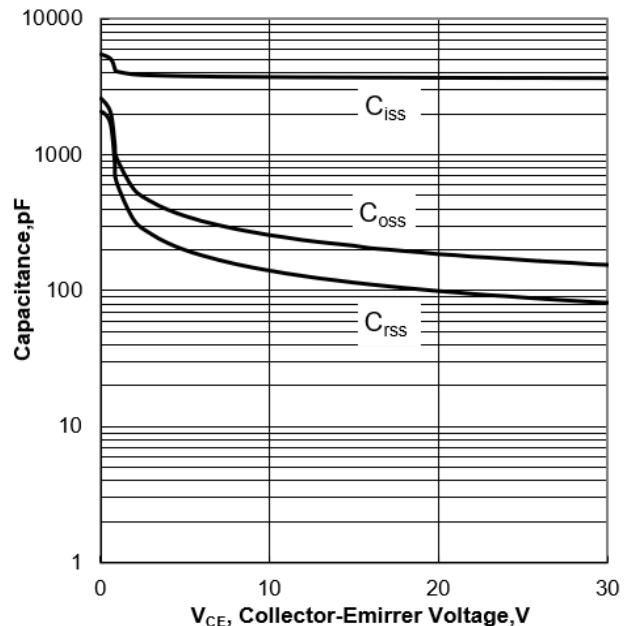


Figure 19. IGBT Transient Thermal Impedance vs Pulse Width

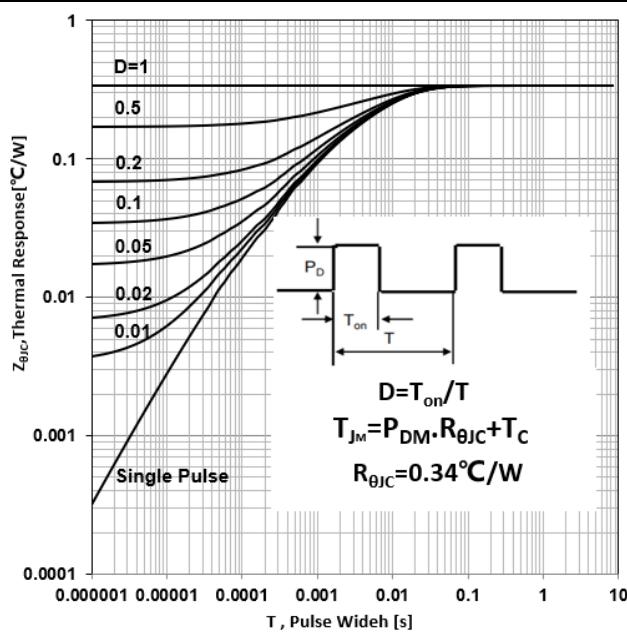
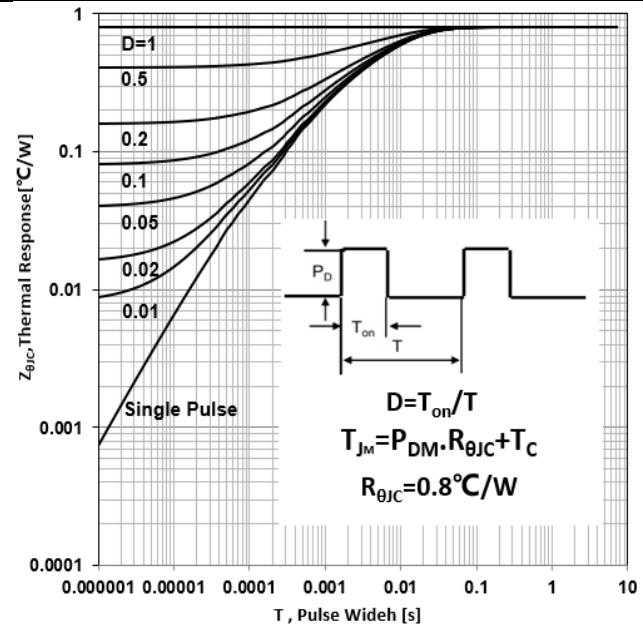


Figure 20. Diode Transient Thermal Impedance vs Pulse Width

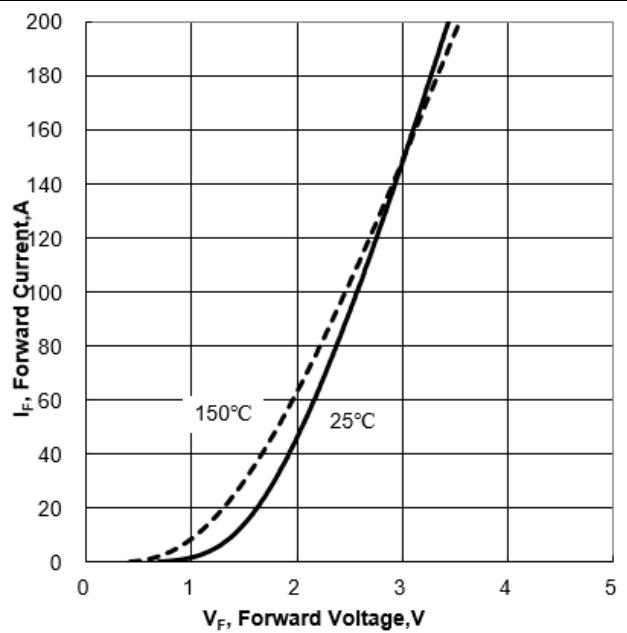




上海捷瑞德半导体
Jerrett Semiconductor

JRG40T120FDH
IGBT

Figure 21. Typical Diode Forward Current vs Forward Voltage





6. Test Circuit and Waveform

Figure 22. Inductive Switching Test Circuit

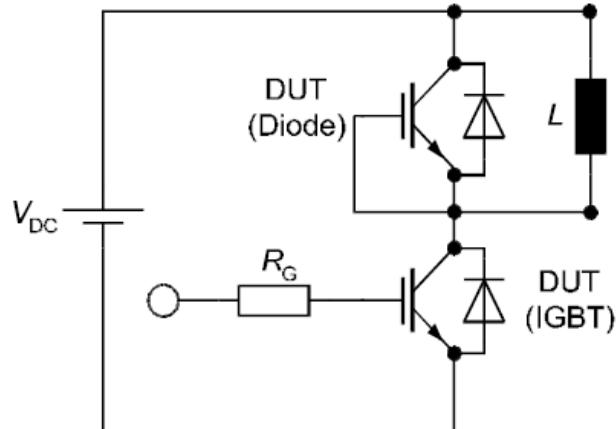


Figure 23. Definition of switching times

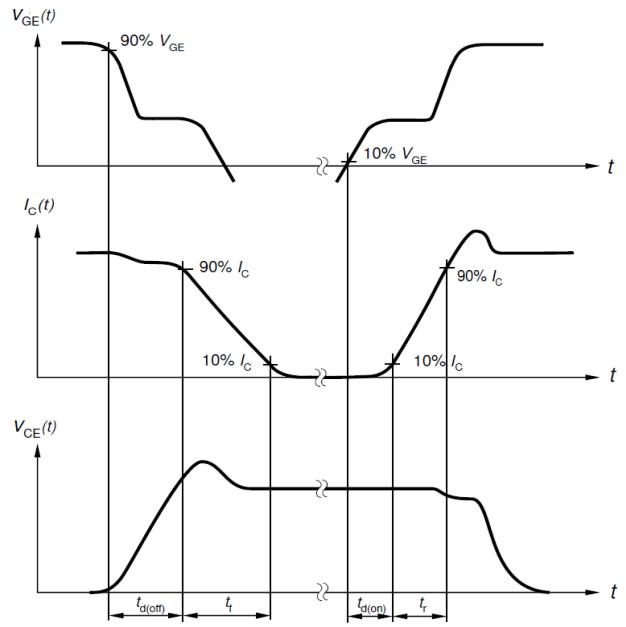


Figure 24. Definition of switching losses

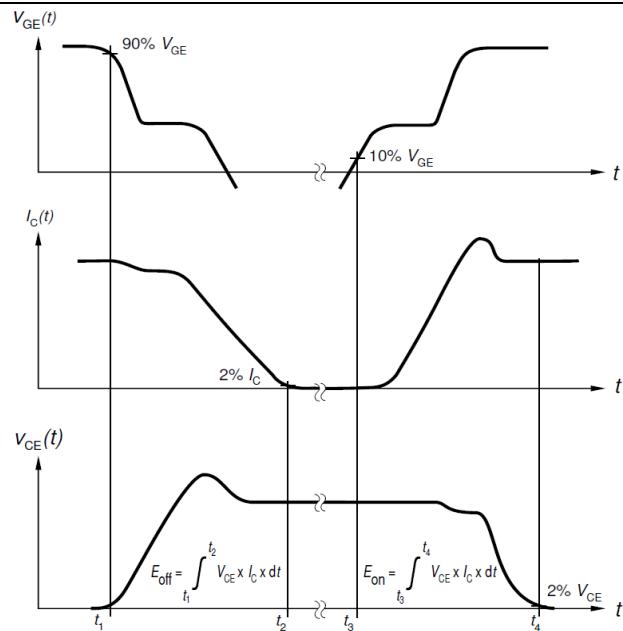
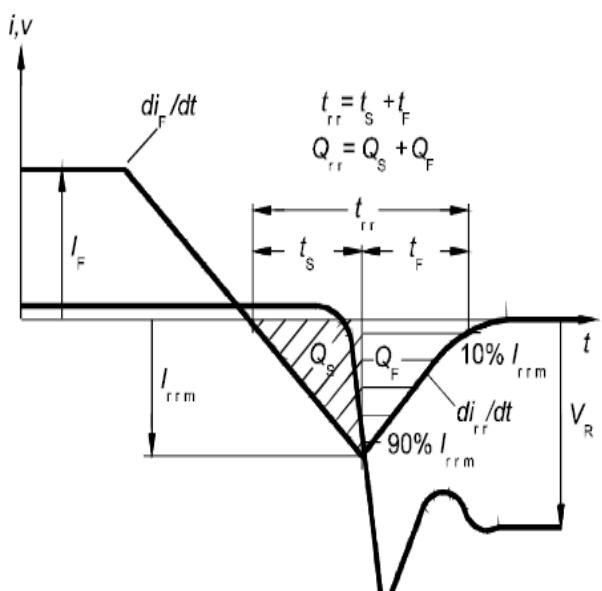


Figure 25. Definition of diode switching characteristics



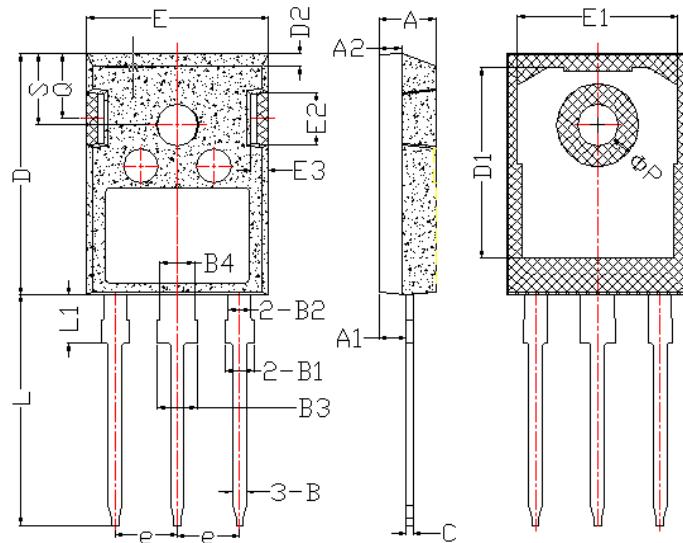


上海捷瑞德半导体
Jerrett Semiconductor

JRG40T120FDH

IGBT

7. Package Description



TO-247 Package

Items	Values(mm)	
	MIN	MAX
A	4.90	5.16
A1	2.27	2.53
A2	1.85	2.11
B	1.07	1.33
B1	1.90	2.41
B2	1.75	2.15
B3	2.87	3.38
B4	2.87	3.13
C	0.55	0.68
D	20.82	21.10
D1	16.25	17.65
D2	1.05	1.35
E	15.70	16.03
E1	13.10	14.15
E2	3.68	5.10
E3	1.68	2.60
e	5.44	
L	19.80	20.31
L1	4.17	4.47
ΦP	3.50	3.70
Q	5.49	6.00
S	6.04	6.30



上海捷瑞德半导体
Jerrett Semiconductor

JRG40T120FDH
IGBT

NOTE:

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. Please do not exceed the absolute maximum ratings of the device when circuit designing.
2. When installing the heat sink, please pay attention to the torsional moment and the smoothness of the heat sink.
3. IGBTs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.