



上海捷瑞德半导体  
Jerrett Semiconductor

**JRG40T65FUL**

IGBT

## 1. Description

JRG40T65FUL is obtained by advanced Trench Field Stop (T-FS) technology which is characteristic with low  $V_{CE(sat)}$ , optimized switching performance and low gate charge  $Q_g$ . The IGBT is suitable device for welding, UPS, and high switching frequency applications.

### KEY CHARACTERISTICS

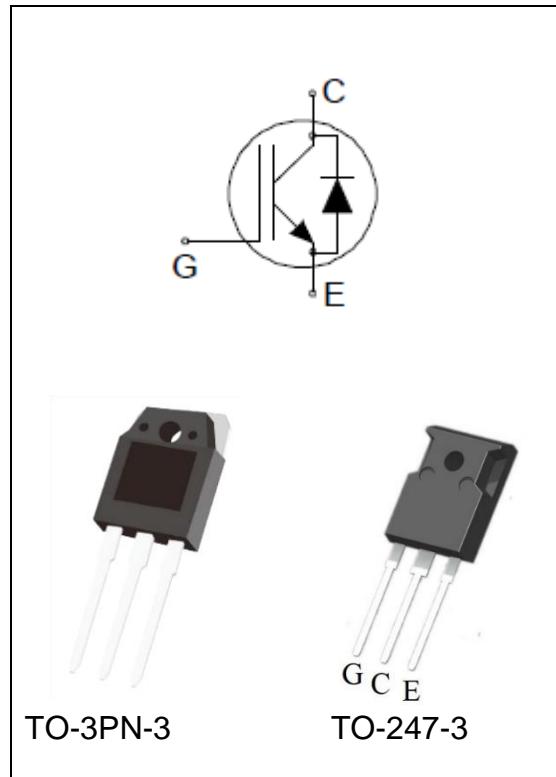
Parameter	Value	Unit
$V_{CES}$	650	V
$I_c$	40	A
$V_{CE(sat).typ}$	1.55	V

### FEATURES

- Fast Switching
- Low  $V_{CE(sat)}$
- Positive temperature coefficient
- Fast recovery anti-parallel diode
- RoHS product

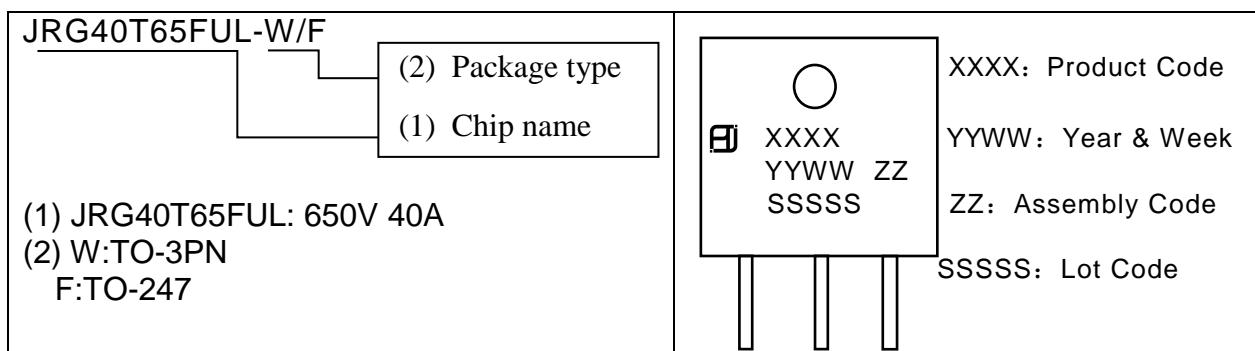
### APPLICATIONS

- Welding converters
- UPS
- Air condition



## ORDERING INFORMATION

Ordering Codes	Package	Product Code	Packing
JRG40T65FUL-W	TO-3PN	G4065FUL	Tube
JRG40T65FUL-F	TO-247	G4065FUL	Tube



## 2. ABSOLUTE RATINGS

Symbol	Parameter	TO-220	Units
$V_{CES}$	Collector-Emitter Voltage	650	V
$I_C$	Collector Current @ $T_c=25^\circ C$	80	A
	Collector Current @ $T_c=100^\circ C$	40	A
$I_{CM}$	Pulsed Collector Current, tp limited by $T_{Jmax}$	160	A
$I_F$	Diode Continuous Forward Current @ $T_c=25^\circ C$	40	A
	Diode Continuous Forward Current @ $T_c=100^\circ C$	20	A
$I_{FM}$	Diode Maximum Forward Current, limited by $T_{Jmax}$	80	A
$V_{GES}$	Gate-Emitter Voltage	$\pm 30$	V
$t_{sc}$	Short circuit withstand time $V_{GE}=15V$ , $V_{CC}\leq 400V$ , Allowed number of short circuits < 1000, Times between short circuits: $\geq 1.0s$ , $T_J \leq 175^\circ C$	3.0	us
$P_D$	Power Dissipation @ $T_c=25^\circ C$	298	W
$T_{Jmax}, T_{stg}$	Operating Junction and Storage Temperature Range	175, -55 to 175	°C
$T_L$	Maximum Temperature for Soldering	260	°C

## 3. Thermal characteristics

Symbol	Parameter	Values	Units
$R_{\theta JC}$	Junction-to-Case (IGBT)	0.5	°C/W
$R_{\theta JC}$	Junction-to-Case (Diode)	0.8	°C/W
$R_{\theta JA}$	Junction-to-Ambient	40	°C/W

## 4. Electrical Characteristics

at  $T_c = 25^\circ C$ , unless otherwise specified

### Static Characteristics

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
$V_{CES}$	Collector-Emitter Breakdown Voltage	$V_{GE} = 0V$ , $I_C = 250\mu A$	650	--	--	V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$V_{GE} = 15V$ , $I_C = 40A$ $T_J=25^\circ C$ $T_J=125^\circ C$ $T_J=175^\circ C$	-- -- --	1.55 1.80 1.90	1.95 -- --	V



上海捷瑞德半导体  
Jerrett Semiconductor

**JRG40T65FUL**

IGBT

$V_{GE(TH)}$	Gate Threshold Voltage	$V_{CE} = V_{GE}, I_C = 1\text{mA}$	5.1	5.8	6.5	V
$V_F$	Diode Forward Voltage	$I_F=20\text{A}$ $T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$ $T_J=175^\circ\text{C}$	-- -- -- --	2.20 1.80 1.52	2.80 -- --	V
$V_F$	Diode Forward Voltage	$I_F=40\text{A}$ $T_J=25^\circ\text{C}$ $T_J=125^\circ\text{C}$ $T_J=175^\circ\text{C}$	-- -- -- --	2.60 2.25 1.95	3.20 -- --	V
$V_{GE(TH)}$	Gate Threshold Voltage	$V_{CE} = V_{GE}, I_C = 1\text{mA}$	4.8	5.5	6.2	V
$I_{CES}$	Collector-Emitter Leakage Current	$V_{CE} = 650\text{V}, V_{GE} = 0\text{V}$	--	--	4	$\mu\text{A}$
$I_{GES(F)}$	Gate-Emitter Forward Leakage Current	$V_{GE} = +30\text{V}$	--	--	200	nA
$I_{GES(R)}$	Gate-Emitter Reverse Leakage Current	$V_{GE} = -30\text{V}$	--	--	-200	nA
Pulse width $tp \leq 300\mu\text{s}$ , $\delta \leq 2\%$						

### Dynamic Characteristics

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
$C_{iss}$	Input Capacitance	$V_{GE}=0\text{V}$ $V_{CE}=25\text{V}$ $f = 1.0\text{MHz}$	--	2225	--	pF
$C_{oss}$	Output Capacitance		--	90	--	
$C_{rss}$	Reverse Transfer Capacitance		--	27	--	
$Q_G$	Gate charge	$V_{CC}=520\text{V}$ $I_{CE}=20\text{A}$ $V_{GE}=15\text{V}$	--	110	--	nC
$Q_{GE}$	Gate-emitter charge		--	52	--	
$Q_{GC}$	Gate-collector charge		--	23	--	
$I_{C(SC)}$	Short circuit collector current Max.1000 short circuits, Times between short circuits: $\geq 1.0\text{s}$	$V_{GE}=15.0\text{V}, V_{CC}\leq 400\text{V}, t_{SC}\leq 3\mu\text{s}, T_J\leq 175^\circ\text{C}$		320		A

### IGBT Switching Characteristics, at $T_J=25^\circ\text{C}$

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
$t_{d(on)}$	Turn-on Delay Time	$I_C = 40\text{A}$ $V_{CE} = 400\text{V}$ $V_{GE} = 15\text{V}$ $R_G = 10\Omega$ $T_J = 25^\circ\text{C}$ Inductive Load	--	30	--	ns
$t_r$	Rise Time		--	66	--	
$t_{d(off)}$	Turn-Off Delay Time		--	138	--	
$t_f$	Fall Time		--	52	--	



上海捷瑞德半导体  
Jerrett Semiconductor

**JRG40T65FUL**

IGBT

$E_{on}$	Turn-On Switching Loss		--	1.0	--	mJ
$E_{off}$	Turn-Off Switching Loss		--	0.7	--	
$E_{ts}$	Total Switching Loss		--	1.7	--	

#### IGBT Switching Characteristics, at $T_J=175^\circ C$

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
$t_{d(on)}$	Turn-on Delay Time	$I_C = 40A$ $V_{CE} = 400V$ $V_{GE} = 15V$ $R_G = 10\Omega$ $T_J = 175^\circ C$ Inductive Load	--	23	--	ns
$t_r$	Rise Time		--	60	--	
$t_{d(off)}$	Turn-Off Delay Time		--	173	--	
$t_f$	Fall Time		--	64	--	
$E_{on}$	Turn-On Switching Loss		--	1.28	--	mJ
$E_{off}$	Turn-Off Switching Loss		--	1.04	--	
$E_{ts}$	Total Switching Loss		--	2.32	--	

#### Diode Characteristics, at $T_J=25^\circ C$

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
$T_{rr}$	Reverse Recovery Time	$I_F = 20A$ , $di/dt = 200A/\mu s$ , $T_J = 25^\circ C$	--	97	--	ns
$Q_{rr}$	Reverse Recovery Charge		--	109	--	nC
$I_{rrm}$	Reverse Recovery Current		--	1.8	--	A
$T_{rr}$	Reverse Recovery Time	$I_F = 40A$ , $di/dt = 200A/\mu s$ , $T_J = 25^\circ C$	--	127	--	ns
$Q_{rr}$	Reverse Recovery Charge		--	326	--	nC
$I_{rrm}$	Reverse Recovery Current		--	4.8	--	A

#### Diode Characteristics, at $T_J=175^\circ C$

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
$T_{rr}$	Reverse Recovery Time	$I_F = 20A$ , $di/dt = 200A/\mu s$ , $T_J = 175^\circ C$	--	147	--	ns
$Q_{rr}$	Reverse Recovery Charge		--	742	--	nC
$I_{rrm}$	Reverse Recovery Current		--	7.8	--	A
$T_{rr}$	Reverse Recovery Time	$I_F = 40A$ , $di/dt = 200A/\mu s$ , $T_J = 175^\circ C$	--	158	--	ns
$Q_{rr}$	Reverse Recovery Charge		--	870	--	nC
$I_{rrm}$	Reverse Recovery Current		--	9	--	A



## 5. Characteristics Curves

Figure 1. Forward Bias Safe Operating Area for TO3PN/TO247

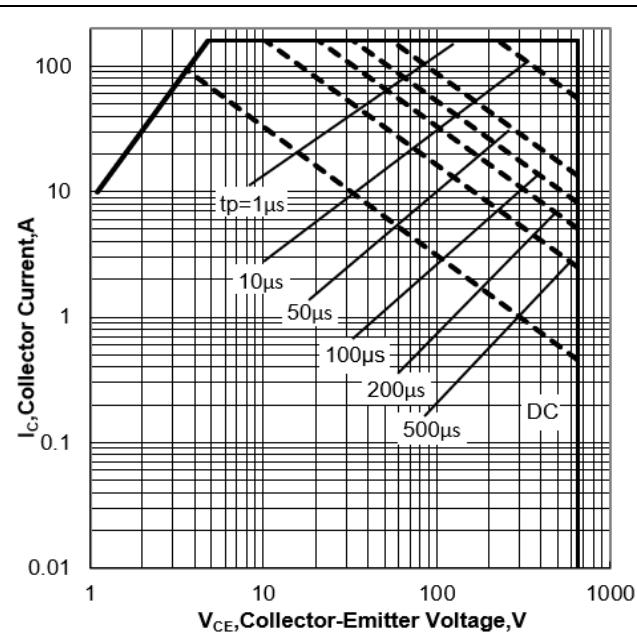


Figure 2. Power Dissipation vs Case Temperature for TO3PN/TO247

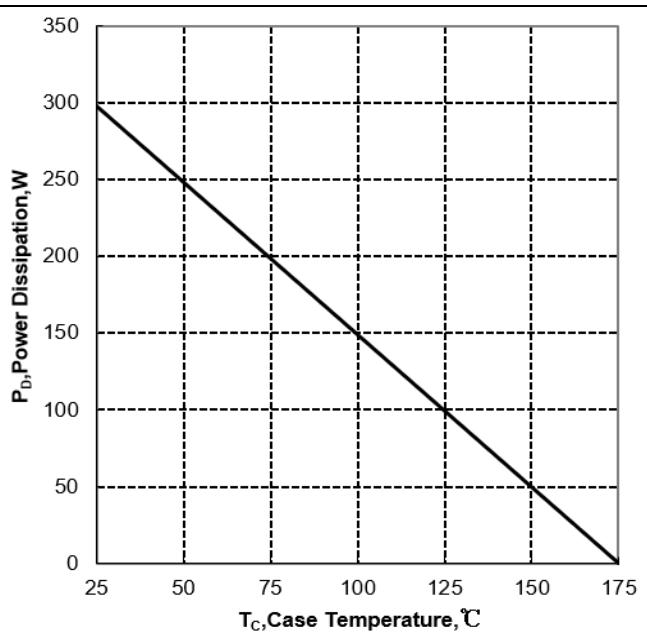


Figure 3. Collector Current vs Case Temperature

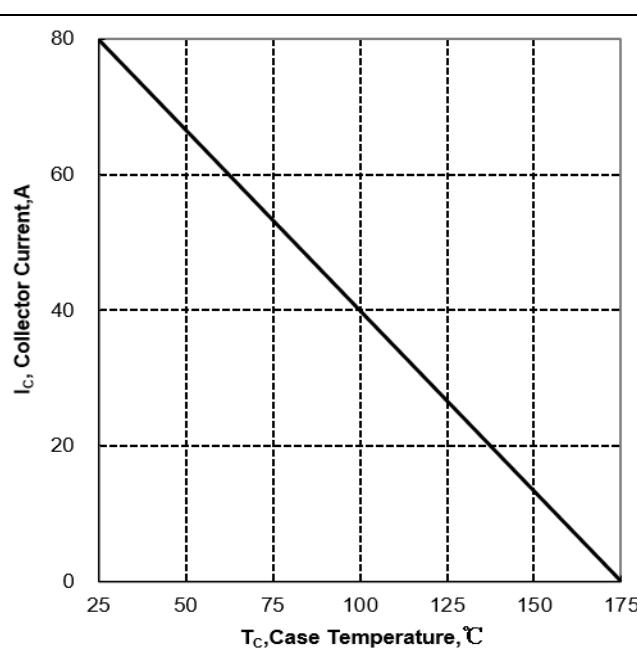


Figure 4. Typical Transfer Characteristics

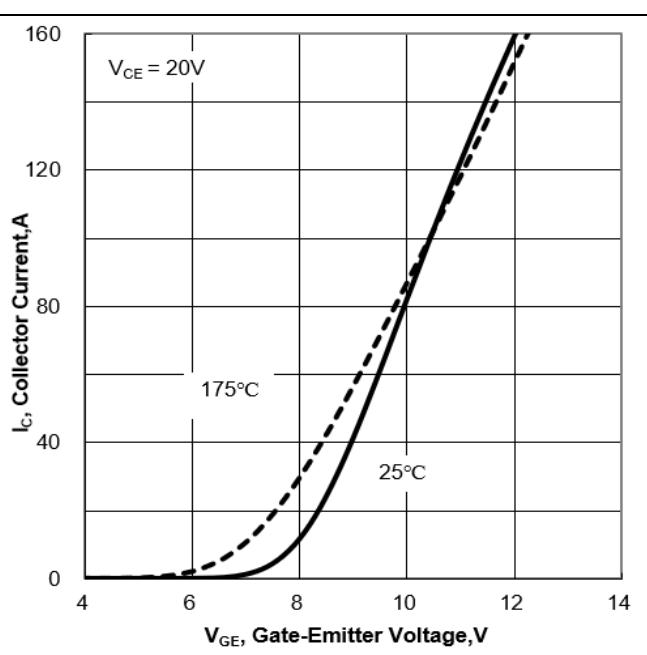




Figure 5. Typical Output Characteristics  
( $T_c=25^\circ\text{C}$ )

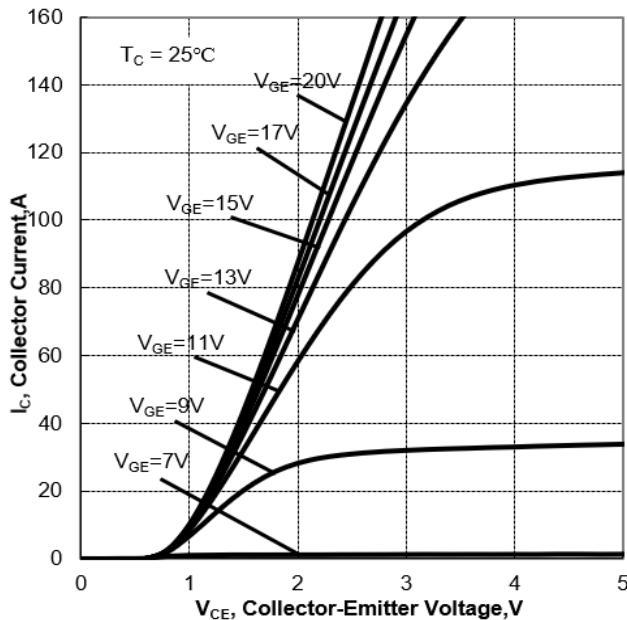


Figure 6. Typical Output Characteristics  
( $T_c=175^\circ\text{C}$ )

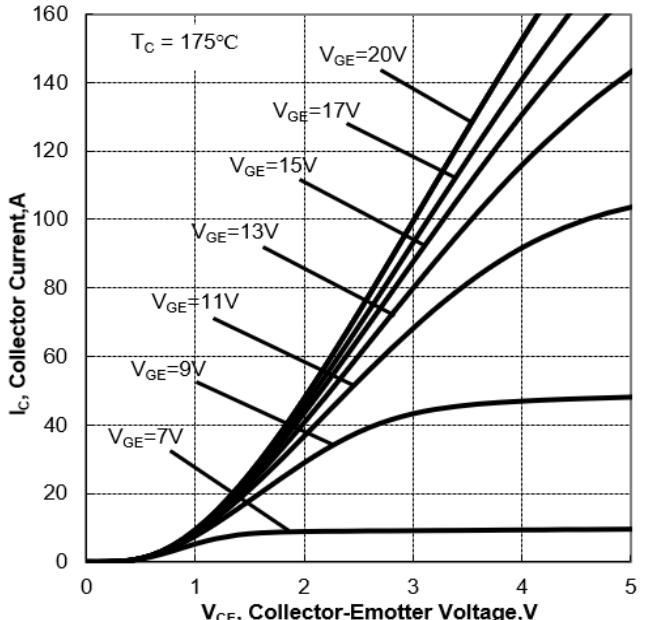


Figure 7. Typical Collector-Emitter Saturation Voltage vs Case Temperature

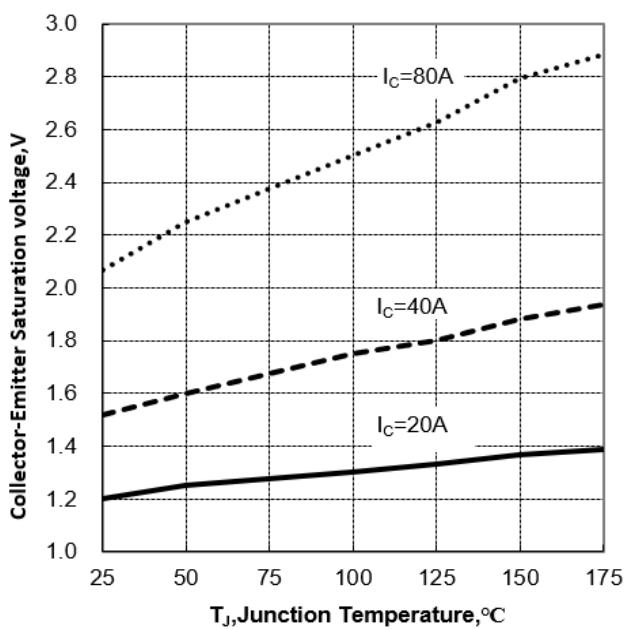
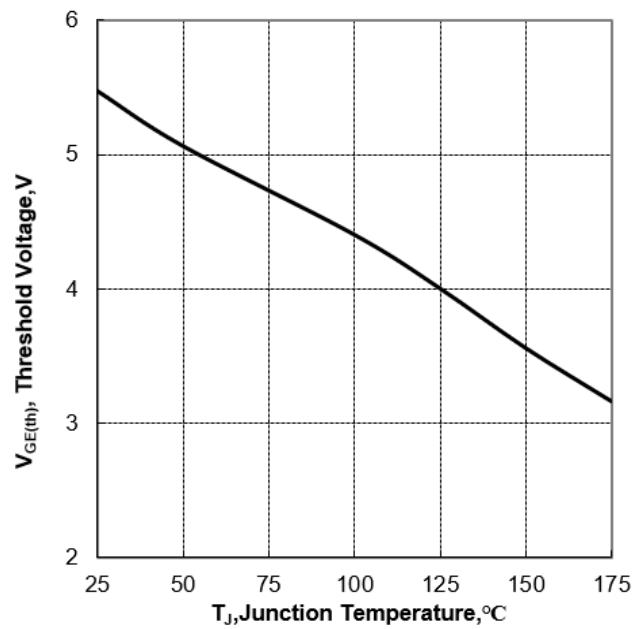


Figure 8. Typical Gate-Emitter Threshold Voltage vs Case Temperature





上海捷瑞德半导体  
Jerrett Semiconductor

# JRG40T65FUL

IGBT

Figure 9. Typical Switching Times vs Gate Resistor ( $T_J=25^\circ\text{C}$ ,  $V_{ce}=400\text{V}$ ,  $V_{ge}=15/0\text{V}$ ,  $I_c=20\text{A}$ )

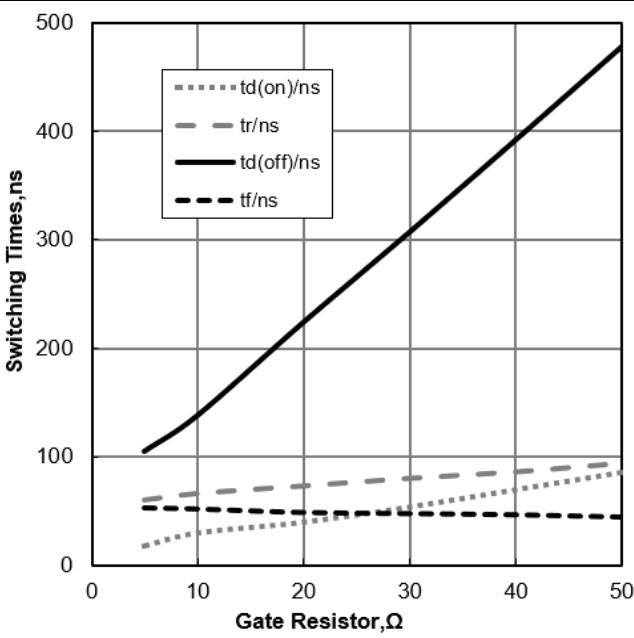


Figure 10. Typical Switching Energy vs Gate Resistor ( $T_J=25^\circ\text{C}$ ,  $V_{ce}=400\text{V}$ ,  $V_{ge}=15/0\text{V}$ ,  $I_c=20\text{A}$ )

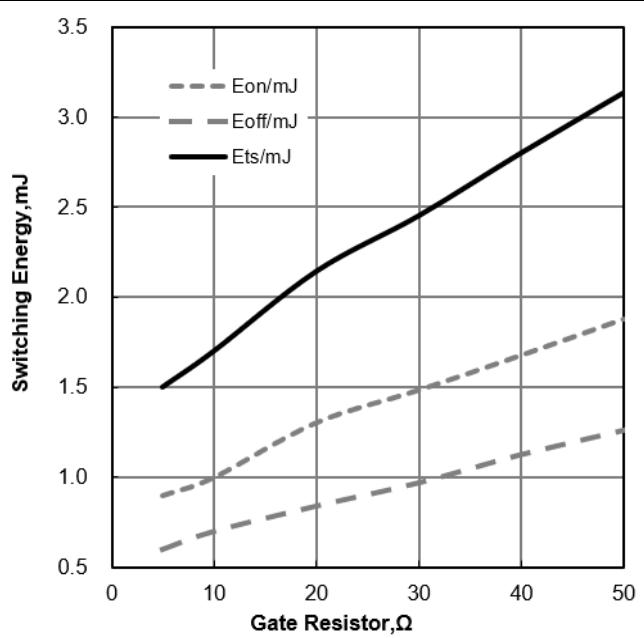


Figure 11. Typical Switching Times vs Case Temperature ( $V_{ce}=400\text{V}$ ,  $V_{ge}=15/0\text{V}$ ,  $I_c=20\text{A}$ )

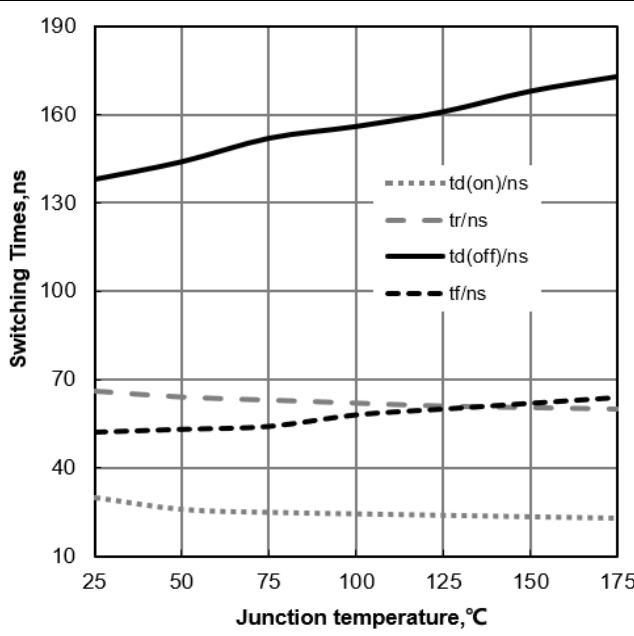
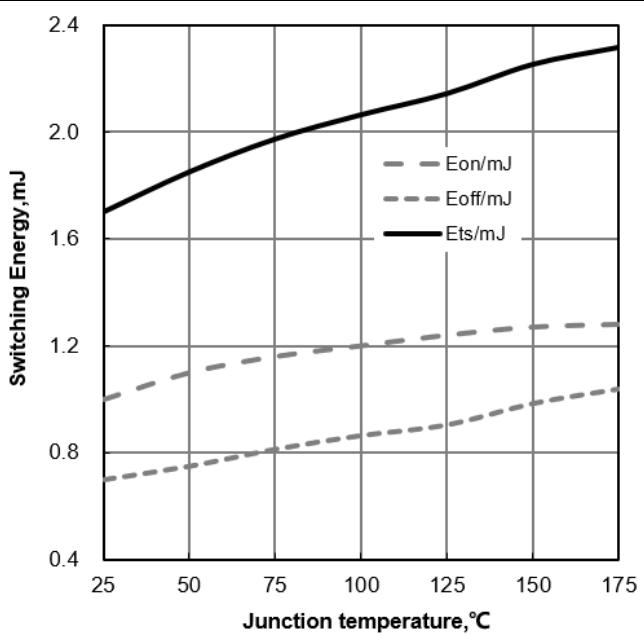


Figure 12. Typical Switching Energy vs Case Temperature ( $V_{ce}=400\text{V}$ ,  $V_{ge}=15/0\text{V}$ ,  $I_c=20\text{A}$ )

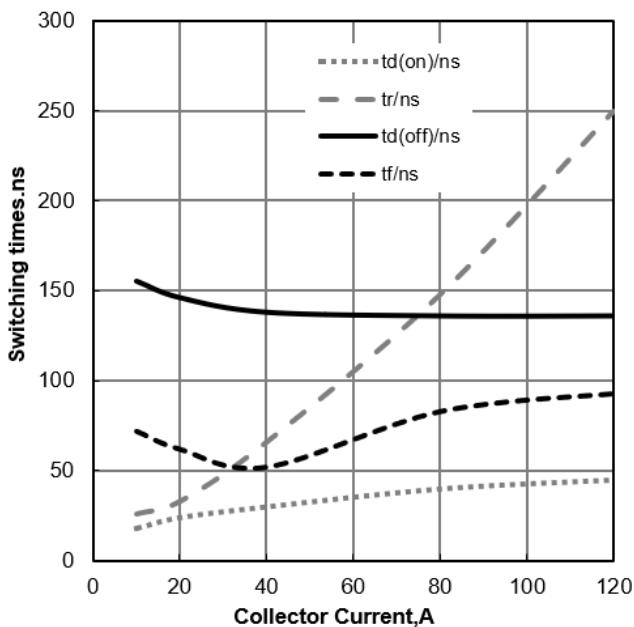




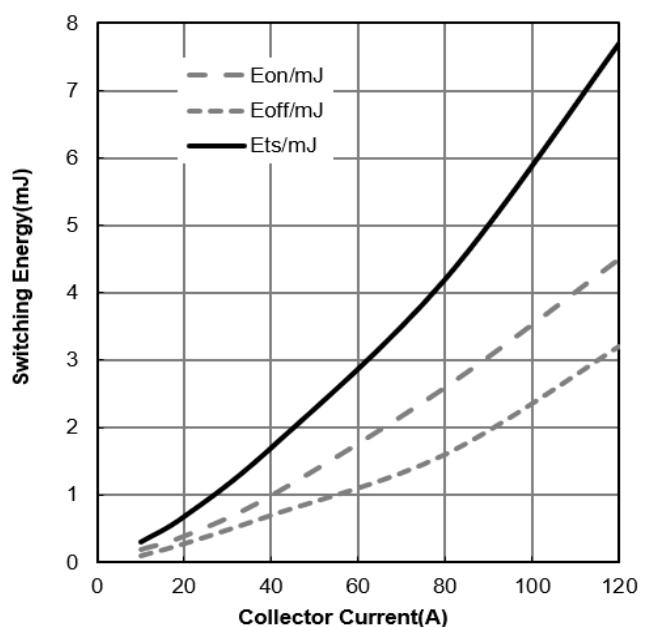
上海捷瑞德半导体  
Jerrett Semiconductor

**JRG40T65FUL**  
IGBT

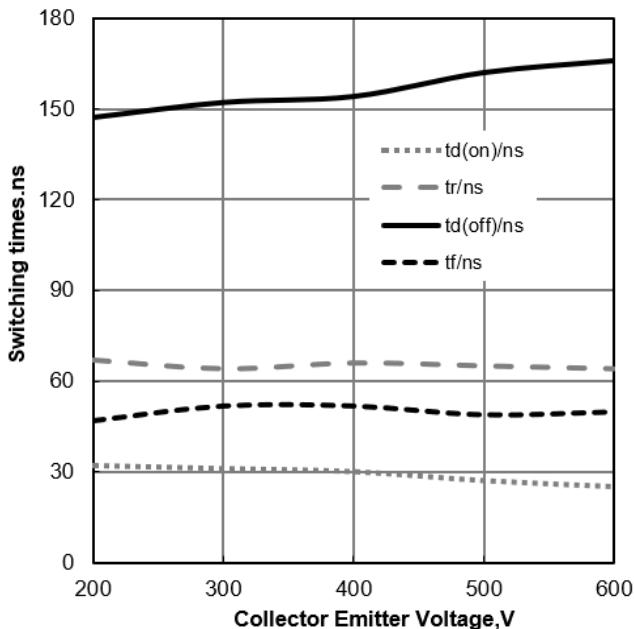
**Figure 13. Typical Switching Times vs Collector Current ( $T_c=25^\circ\text{C}$ ,  $V_{ce}=400\text{V}$ ,  $V_{ge}=15/0\text{V}$ )**



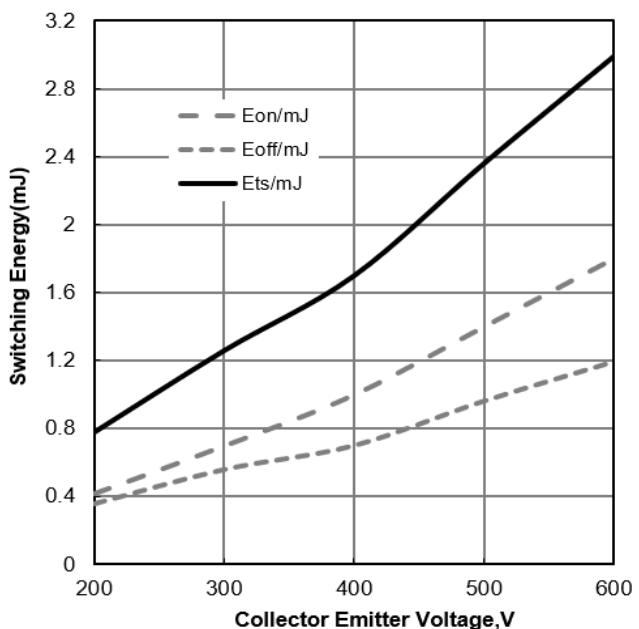
**Figure 14. Typical Switching Energy vs Collector Current ( $T_c=25^\circ\text{C}$ ,  $V_{ce}=400\text{V}$ ,  $V_{ge}=15/0\text{V}$ )**



**Figure 15. Typical Switching Times vs  $V_{ce}$  ( $T_c=25^\circ\text{C}$ ,  $V_{ge}=15/0\text{V}$ ,  $I_c=20\text{A}$ )**



**Figure 16. Typical Switching Energy vs  $V_{ce}$  ( $T_c=25^\circ\text{C}$ ,  $V_{ge}=15/0\text{V}$ ,  $I_c=20\text{A}$ )**





上海捷瑞德半导体  
Jerrett Semiconductor

JRG40T65FUL  
IGBT

Figure 17. Typical Gate Charge

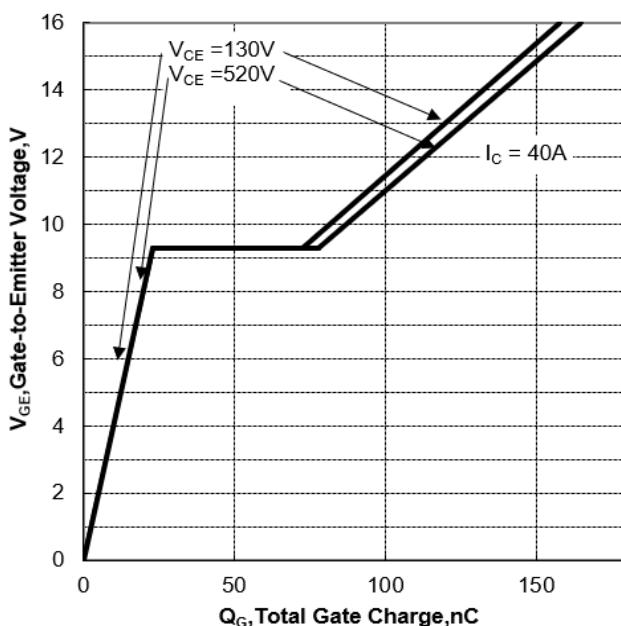


Figure 18. Typical Capacitance vs Collector-Emitter Voltage

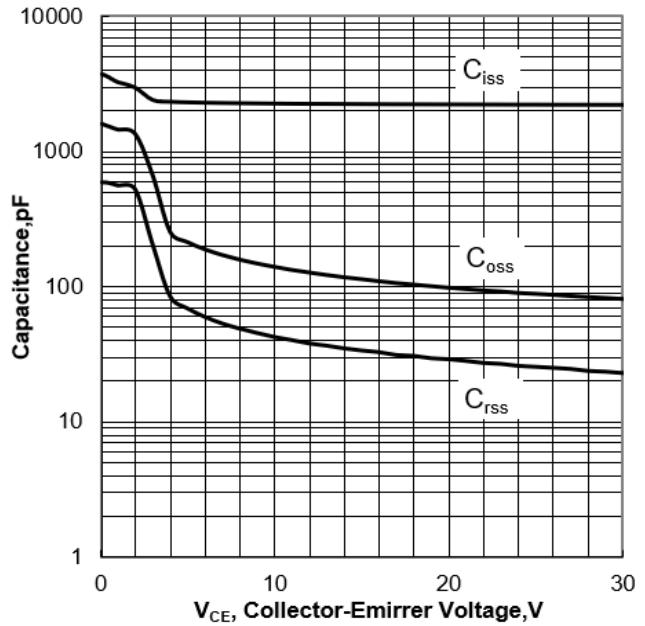


Figure 19. IGBT Transient Thermal Impedance vs Pulse Width(TO3PN/TO247)

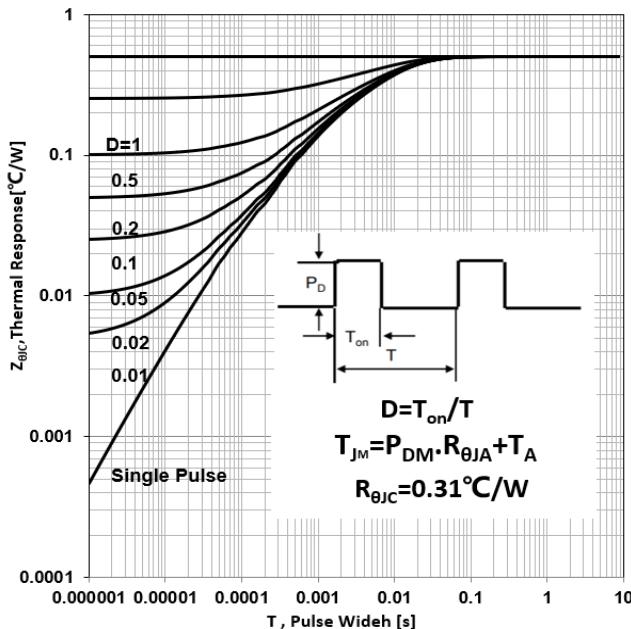
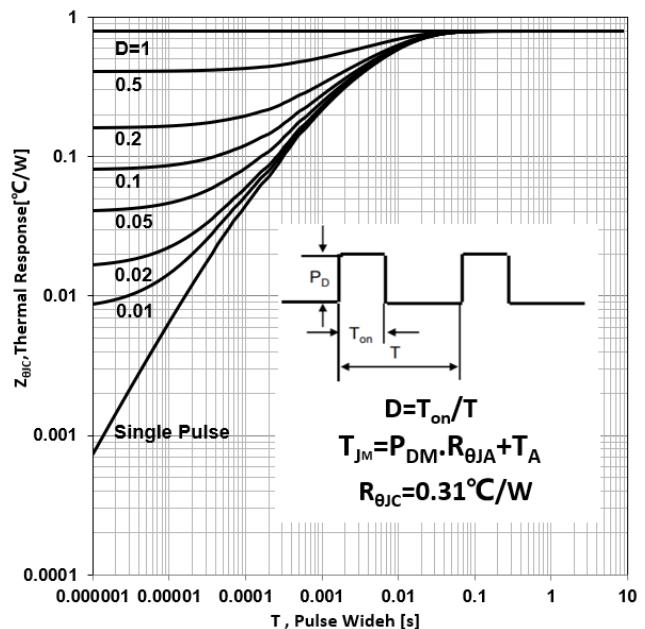


Figure 20. Diode Transient Thermal Impedance vs Pulse Width(TO3PN/TO247)

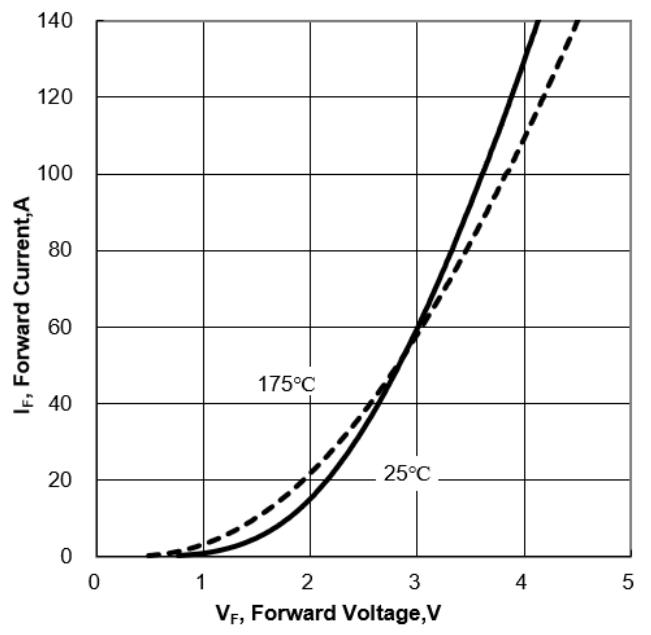




上海捷瑞德半导体  
Jerrett Semiconductor

**JRG40T65FUL**  
IGBT

**Figure 21. Typical Diode Forward Current vs Forward Voltage**





## 6. Test Circuit and Waveform

Figure 11. Inductive Switching Test Circuit

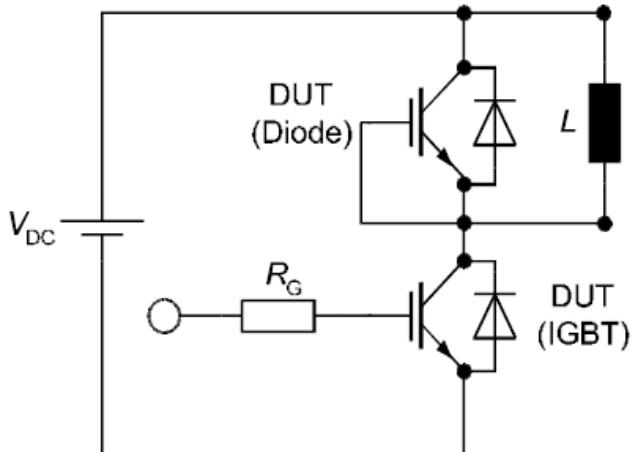


Figure 12. Definition of switching times

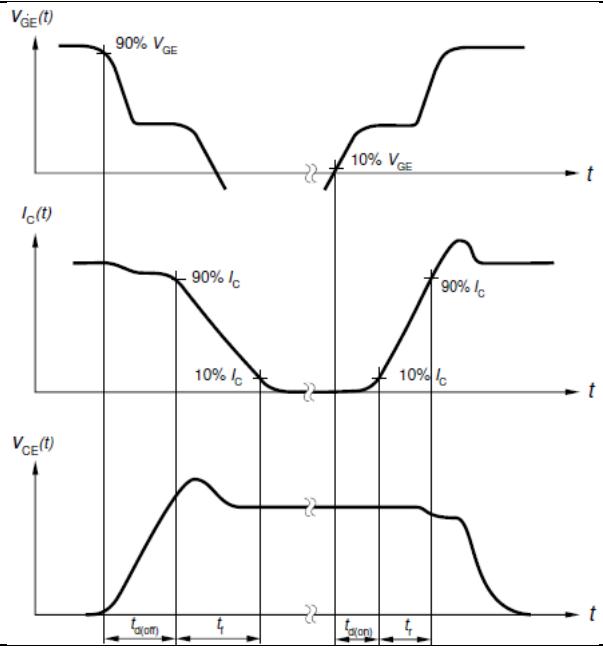


Figure 13. Definition of switching losses

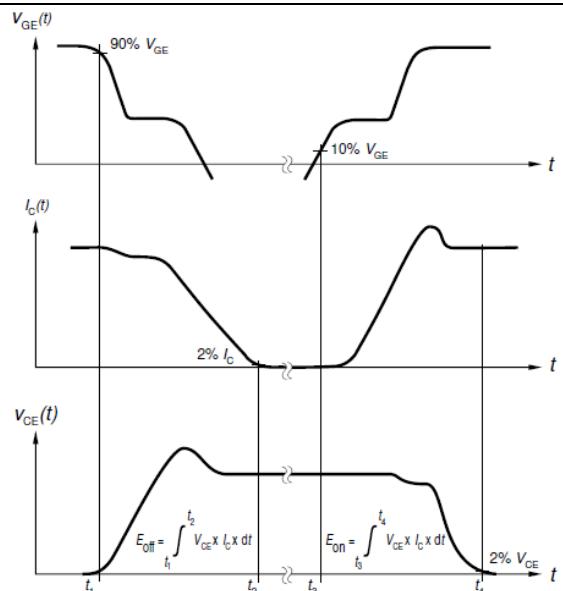
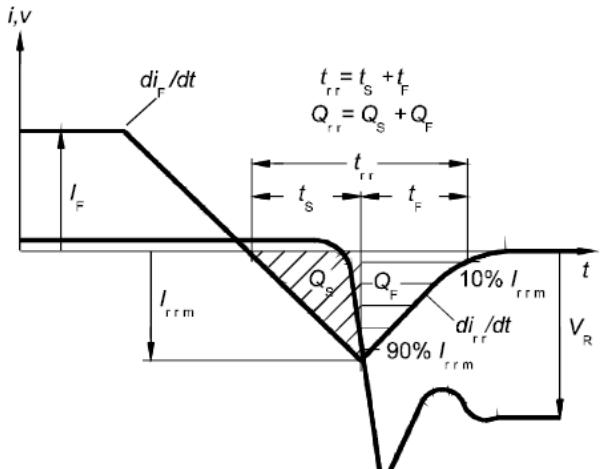
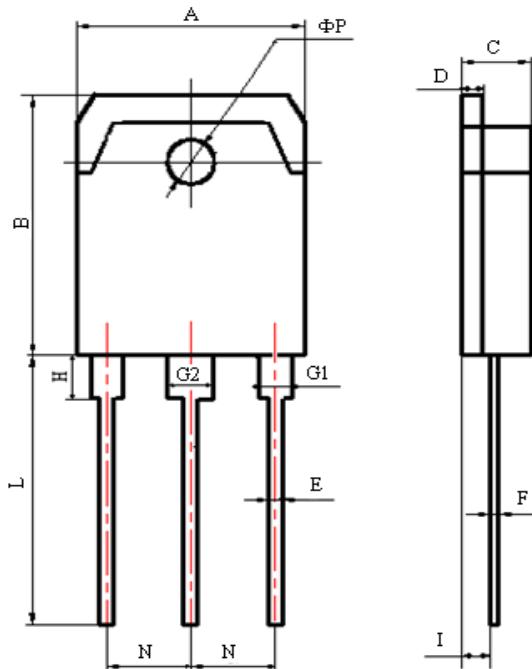


Figure 14. Definition of diode switching characteristics



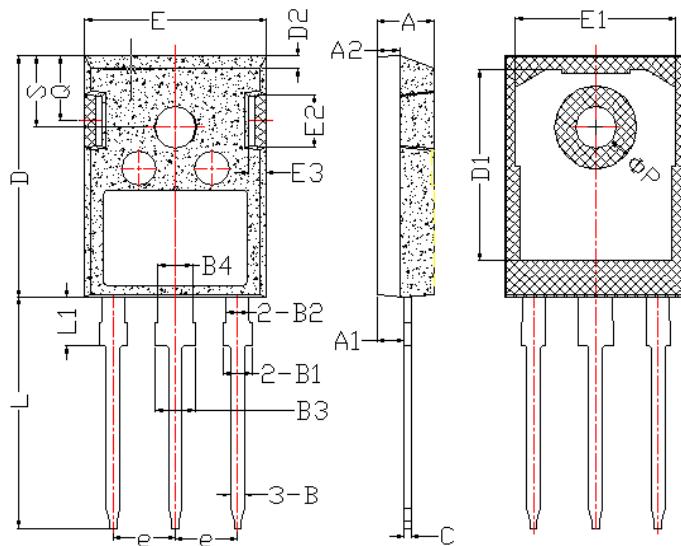


## 7. Package Description



TO-3PN Package

Items	Values(mm)	
	MIN	MAX
A	15.00	16.00
B	19.20	20.60
C	4.60	5.00
D	1.40	1.60
E	0.90	1.10
F	0.50	0.70
G1	2.00	2.20
G2	3.00	3.20
H	3.00	3.70
I	1.20	1.70
	2.70	2.90
L	19.00	21.00
N	5.25	5.65
Φ P	3.10	3.30



TO-247 Package

Items	Values(mm)	
	MIN	MAX
A	4.90	5.16
A1	2.27	2.53
A2	1.85	2.11
B	1.07	1.33
B1	1.90	2.41
B2	1.75	2.15
B3	2.87	3.38
B4	2.87	3.13
C	0.55	0.68
D	20.82	21.10
D1	16.25	17.65
D2	1.05	1.35
E	15.70	16.03
E1	13.10	14.15
E2	3.68	5.10
E3	1.68	2.60
e	5.44	
L	19.80	20.31
L1	4.17	4.47
ΦP	3.50	3.70
Q	5.49	6.00
S	6.04	6.30



上海捷瑞德半导体  
Jerrett Semiconductor

**JRG40T65FUL**  
**IGBT**

**NOTE:**

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. Please do not exceed the absolute maximum ratings of the device when circuit designing.
2. When installing the heat sink, please pay attention to the torsional moment and the smoothness of the heat sink.
3. MOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.